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October 2, 2000

Box New Applications

Assistant Commissioner for Patents
Washington, DC 20231



Re: New Utility Non-Provisional U.S. Patent Application
Application No. **TO BE ACCORDED** Filed **HEREWITH**
Title: **LIQUID CRYSTAL DISPLAY**
Inventors: **Jang-Kun SONG and Jae-Jin RYU**
Our Ref: **06192.0154.NPUS00**

Sir:

The following documents are forwarded herewith for appropriate action by the U.S. Patent and Trademark Office:

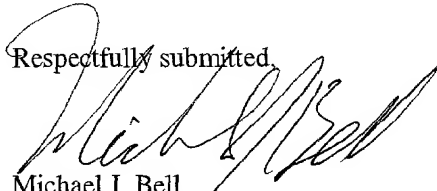
1. U.S. Utility Patent Application entitled:
LIQUID CRYSTAL DISPLAY
and naming as inventors: **Jang-Kun SONG and Jae-Jin RYU**
the application consisting of:
 - a. A specification containing:
 - (i) **43** pages of description prior to the claims;
 - (ii) **20** pages of claims (**71** claims);
 - (iii) a **one** (1) page abstract;
 - b. **41** sheets of drawings: (**Figures 1-28**);
2. Form PTO-SB/05;
3. Form PTO 1082;
4. Certified copy of Korean Patent Application No. 99-42216 claiming priority to October 1, 1999; and
5. An executed combined Declaration and Power of Attorney;
7. Two (2) return postcards; and
8. Our check No. **337096** for **\$2,428.00** to cover:
\$ **710.00** Filing fee for patent application;
\$ **1,718.00** Fee for excess claims.

Box New Applications
Attorney Docket No. 06192.0154.NPUS00
October 2, 2000
Page 2

It is respectfully requested that, of the two attached postcards, one be stamped with the filing date of these documents and returned to our courier, and the other, prepaid postcard, be stamped with the filing date and unofficial application number and returned as soon as possible.

The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 08-3038 referencing docket number 06192.0154.NPUS00. *A duplicate copy of this letter is enclosed.*

Respectfully submitted,


Michael J. Bell
Registration No. 39,604

Enclosures

HOWREY SIMON ARNOLD & WHITE, LLP**Box No. 34****1299 Pennsylvania Avenue, NW****Washington, DC 20004-2402****(202) 783-0800**Attorney Docket No. **06192.0154.NPUS00**

ASSISTANT COMMISSIONER FOR PATENTS

Washington, DC 20231

Sir:

Transmitted herewith for filing is the patent application of

Inventor: **Jung-Kun SONG et al**For: **LIQUID CRYSTAL DISPLAY**

jc917 U.S. PTO
09/676812
10/02/00

Enclosed is a new application comprising:

- ☒ A specification containing: 43 pages of description prior to the claims; 20 page of claims (**71** claims); a one (1) page abstract; 41 sheets of drawings (Figures 1-28)
- ☒ PTO SB/05
- ☒ Executed Combined Declaration and Power of Attorney
- ☒ Executed Assignment and Recordation Cover Sheet
- ☒ Certified Copy of Korean Patent Application 99-42216 claiming priority to October 1, 2000.

The filing fee has been calculated as shown below:

(Col 1)		(Col. 2)	SMALL ENTITY		OR	OTHER THAN A SMALL ENTITY	
FOR	NO. FILED	NO. EXTRA	RATE			RATE	FEE
BASIC FEE				\$ 345.00	OR		\$710.00
TOTAL CLAIMS	71-20 =	* 51	x 9 =		OR	x 18 =	918.00
INDEP. CLAIMS	13-3 =	* 10	x 39 =		OR	x 80 =	800.00
<input type="checkbox"/> MULTIPLE DEPENDENT CLAIM PRESENTED			+ 130 =		OR	+ 270 =	
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- ☒ The U.S. Patent and Trademark Office is hereby authorized to charge payment of the following fees during the pendency of this application or credit any overpayment to Deposit Account No. 08-3038 referencing docket number 06192.0154.NPUS00. A duplicate copy of this sheet is enclosed.
- ☒ Any patent application processing fees under 37 CFR 1.17
- ☒ Any filing fees under 37 CFR 1.16 for presentation of extra claims.

Date

10/2/00

Michael J. Bell

Registration No. 39 604

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PTO/SB/05 (2/98)

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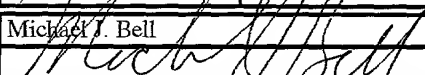
UTILITY PATENT APPLICATION TRANSMITTAL <small>(Only for new nonprovisional applications under 37 CFR 1.53(b))</small>		Attorney Docket No. 06192.0154.NPUS00	
		First Named Inventor or Application Identifier Jang-Kun SONG	
		Title Liquid Crystal Display	
		Express Mail Label No.	

APPLICATION ELEMENTS <small>See MPEP chapter 600 concerning utility patent application contents</small>	ADDRESS TO: Assistant Commissioner for Patent Box Patent Application Washington, DC 20231
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<p>1. <input checked="" type="checkbox"/> *Fee Transmittal Form (Form PTO-1082) <small>(Submit an original and a duplicate for fee processing)</small></p> <p>2. <input checked="" type="checkbox"/> Specification [Total Pages 64] <small>(preferred arrangement set forth below)</small></p> <ul style="list-style-type: none">- Descriptive title of the Invention- Cross References to Related Applications- Statement Regarding Fed sponsored R&D- Reference to Microfiche Appendix- Background of the Invention- Brief Summary of the Invention- Brief Description of the Drawings (if filed)- Detailed Description- Claims- Abstract of the Disclosure <p>3. <input checked="" type="checkbox"/> Drawing(s) (35 USC 113) [Total Sheets 41]</p> <p>4. <input type="checkbox"/> Oath or Declaration [Total Pages 3]</p> <p>a. <input checked="" type="checkbox"/> Newly executed (original or copy)</p> <p>b. <input type="checkbox"/> Copy from a prior application (37 CFR 1.63(d)) <small>(for continuation/divisional with Box 17 completed) [Note Box 5 below]</small></p> <p>i. <input type="checkbox"/> DELETION OF INVENTOR(S) <small>Signed statement attached deleting inventor(s) named in the prior application, see 37 CFR 1.63(d)(2) and 1.33(b).</small></p> <p>5. <input type="checkbox"/> Incorporation By Reference (useable if Box 4b is checked) <small>The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied under Box 4b, is considered as being part of the disclosure of the accompanying application and is hereby incorporated by reference therein.</small></p>	<p>6. <input type="checkbox"/> Microfiche Computer Program (Appendix)</p> <p>7. <input type="checkbox"/> Nucleotide and/or Amino Acid Sequence Submission <small>(if applicable, all necessary)</small></p> <p>a. <input type="checkbox"/> Computer Readable Copy</p> <p>b. <input type="checkbox"/> Paper Copy (identical to computer copy)</p> <p>c. <input type="checkbox"/> Statement verifying identity of above copies</p> <table border="1" style="width: 100%; border-collapse: collapse;"><tr><td colspan="2" style="text-align: center;">ACCOMPANYING APPLICATION PARTS</td></tr><tr><td>8. <input checked="" type="checkbox"/> Assignment Papers (cover sheet & document(s))</td><td></td></tr><tr><td>9. <input type="checkbox"/> 37 CFR 3.73(b) Statement <input type="checkbox"/> Power of Attorney <small>(when there is an assignee)</small></td><td></td></tr><tr><td>10. <input type="checkbox"/> English Translation Document (if applicable)</td><td></td></tr><tr><td>11. <input type="checkbox"/> Information Disclosure Statement (IDS)/PTO-1449 <input type="checkbox"/> Copies of IDS Citations</td><td></td></tr><tr><td>12. <input type="checkbox"/> Preliminary Amendment</td><td></td></tr><tr><td>13. <input checked="" type="checkbox"/> Return Receipt Postcard (MPEP 503) (Two) <small>(should be specifically itemized)</small></td><td></td></tr><tr><td>14. <input type="checkbox"/> *Small Entity Statement(s) <input type="checkbox"/> Statement filed in prior application, Status still proper and desired</td><td></td></tr><tr><td>15. <input checked="" type="checkbox"/> Certified Copy of Priority Document(s) <small>(if foreign priority is claimed)</small></td><td></td></tr><tr><td>16. <input type="checkbox"/> Other:</td><td></td></tr></table> <p><small>*NOTE FOR ITEMS 1 & 14: IN ORDER TO BE ENTITLED TO PAY SMALL ENTITY FEES, A SMALL ENTITY STATEMENT IS REQUIRED (37 C.F.R. § 1.27), EXCEPT IF ONE FILED IN A PRIOR APPLICATION IS RELIED UPON (37 C.F.R. § 1.28).</small></p>	ACCOMPANYING APPLICATION PARTS		8. <input checked="" type="checkbox"/> Assignment Papers (cover sheet & document(s))		9. <input type="checkbox"/> 37 CFR 3.73(b) Statement <input type="checkbox"/> Power of Attorney <small>(when there is an assignee)</small>		10. <input type="checkbox"/> English Translation Document (if applicable)		11. <input type="checkbox"/> Information Disclosure Statement (IDS)/PTO-1449 <input type="checkbox"/> Copies of IDS Citations		12. <input type="checkbox"/> Preliminary Amendment		13. <input checked="" type="checkbox"/> Return Receipt Postcard (MPEP 503) (Two) <small>(should be specifically itemized)</small>		14. <input type="checkbox"/> *Small Entity Statement(s) <input type="checkbox"/> Statement filed in prior application, Status still proper and desired		15. <input checked="" type="checkbox"/> Certified Copy of Priority Document(s) <small>(if foreign priority is claimed)</small>		16. <input type="checkbox"/> Other:	
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Prior Application Information: Examiner:			Group/Art Unit:

18. CORRESPONDENCE ADDRESS					
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Name (Print/Type)	Michael J. Bell		Registration No. (Attorney/Agent)	39,604	
Signature			Date	October 2, 2000	

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LIQUID CRYSTAL DISPLAY

BACKGROUND OF THE INVENTION

(a) Field of the Invention

5 The present invention relates to a liquid crystal display (LCD) and, more particularly, to an LCD in which a predetermined opening pattern is formed at pixel and common electrodes such that a wide viewing angle is obtained.

(b) Description of the Related Art

Generally, an LCD has a structure having a liquid crystal layer that is sandwiched between two substrates. An electric field is applied to the liquid crystal layer to control the alignment of the liquid crystal molecules, ultimately controlling the transmittance of incident light. In a vertically aligned (VA) LCD, the liquid crystal molecules are aligned perpendicular to the substrates when an electric field is not applied. In case two polarizer films are arranged with their polarizing directions perpendicular to each other, the linearly polarized light passing through the first polarizer film is completely blocked by the second polarizer film in the absence of an electric field. The complete blockage of lights exhibits a very low brightness in an "off" state of the normally black mode. This helps a VA LCD obtain a relatively higher contrast ratio than that of the conventional TN liquid crystal display.

20 However, the liquid crystal molecules are irregularly inclined against the substrate when an electric field is applied. Therefore, in one or more areas, the long axis directions of some of the liquid crystal molecules are aligned with the polarizing direction

of the first polarizer film or the second polarizer film. In such areas, the liquid crystal molecules cannot rotate the polarizing direction, i.e., polarization, and the light is completely blocked by the polarizer film. Such areas appear as black portions on the screen, which degrade the in picture quality. These areas are referred to as areas of “texture.”

In order to solve the above problem, several techniques of electrode-patterning have been suggested. However, a slow response time still remains as a problem.

Fig. 1 illustrates a schematic view of opening patterns formed at pixel and common electrodes in a prior art liquid crystal display. As shown in Fig. 1, the pixel and common electrodes are formed with opening patterns 1 and 2, respectively. Each of the opening patterns 1 and 2 is formed in a V-shape and is arranged with ends of the V-shapes in proximity to each other so that roughly a diamond shape is formed by the opening patterns 1 and 2. Liquid crystal material is injected between the pixel electrode and the common electrode, and liquid crystal molecules 3 are aligned perpendicular to the electrodes.

When an electric field is applied to the liquid crystal material, the liquid crystal molecules 3 come to be arranged parallel to the electrodes. However, the response speed of the liquid crystal molecules 3 with respect to the applied electric field is very slow with the formation of the opening patterns 1 and 2 at the pixel and common electrodes. That is, as a result of a fringe field formed due to the opening patterns 1 and 2, the liquid crystal molecules 3 are first arranged perpendicular to the opening patterns 1 and 2 (A state), then are aligned to be parallel with one another (B state), because liquid

crystal molecules tend to align themselves roughly parallel along their long axes. These two steps of alignment slow down the response speed.

The slow response speed of liquid crystal molecules generates after-images when displaying moving pictures on the screen. There is therefore a need to increase the response speed of liquid crystal molecules.

SUMMARY OF THE INVENTION

It is an object of the present invention to provide a liquid crystal display which has an improved response speed and a wide viewing angle.

It is another object of the present invention to provide a liquid crystal display that shows improved picture images.

These and other objects may be achieved by a liquid crystal display having a first insulating substrate with top and bottom surfaces. A pixel electrode is formed on the top surface of the first insulating substrate. The pixel electrode has a first opening pattern at each pixel area. The pixel electrode with the first opening pattern is substantially rectangular in shape and having first and second long sides, and first and second short sides. The pixel electrode is divided into an upper region defined by the first and second long sides and the first short side, and a lower region defined by the first and second long sides and the second short side. A second insulating substrate with top and bottom surfaces is arranged parallel to the first insulating substrate at a predetermined distance from the same such that the bottom surface of the second insulating substrate faces the top surface of the first insulating substrate. A common electrode is formed on the bottom surface of the second insulating substrate. The common electrode has a second opening pattern at each pixel area, which corresponds to each pixel area of the pixel

electrode. A liquid crystal layer is sandwiched between the first and second substrates while contacting the pixel and common electrodes.

The first and second opening patterns each have a plurality of openings, the openings of the first and second opening patterns being alternately arranged parallel to each other.

The first and second opening patterns each have a middle linear portion. The linear portions of the first and second opening patterns are alternately arranged parallel to each other. The first opening pattern includes a first opening formed in the upper region of the pixel electrode in a first direction. A second opening portion is formed in the lower region of the pixel electrode in a second direction normal to the first direction. The second opening pattern includes a first trunk opening formed in the upper region of the common electrode in the first direction, and a second trunk opening formed in the lower region of the common electrode in the second direction. The first direction is slanted at a predetermined angle with respect to the long or short sides of the pixel electrode. The second opening pattern further includes first branch openings overlapping the first and second short sides of the pixel electrode, and second branch openings overlapping the first and second long sides of the pixel electrode. The first opening pattern further includes a third opening formed where the upper and lower regions of the pixel electrode meet while proceeding parallel to the first and second short sides of the pixel electrode. The second branch openings each have a width greater than that of the trunk opening portion. The first direction is parallel to one of the long and short sides of the pixel electrode. The first and second trunk openings each have opposite ends respectively with a gradually enlarged width. One of the second trunk openings overlaps

the second short side of the pixel electrode. The first opening has opposite ends respectively with a gradually reduced width.

The pixel and common electrodes are overlapped with each other such that the first and second opening patterns partition the pixel electrode into several micro-regions.

5 The micro-regions of the pixel electrode are in the shape of polygons where the longest sides are parallel to each other. The micro-regions of the pixel electrode are classified into a first type where the longest sides are arranged in a first direction, and a second type where the longest sides are arranged in a second direction normal to the first direction. The first direction is slanted at a predetermined angle with respect to the long or short sides of the pixel electrode. Alternatively, the first direction may be parallel to one of the long and short sides of the pixel electrode.

The first and second opening patterns form fringe fields when voltage is applied between the pixel and common electrodes. The orienting direction of the liquid crystal molecules due to the fringe fields corresponds to that of the liquid crystal molecules as a result of a force exerted by the molecules. It is preferable that the liquid crystal molecules are oriented in four directions. The opening width of the first and second opening patterns is preferably in the range of 10-16 μ m.

The pixel electrode may have protrusions at the sides adjacent to the ends of the first and second openings.

BRIEF DESCRIPTION OF THE DRAWINGS

A more complete appreciation of the invention, and many of the attendant advantages thereof, will be readily apparent as the same becomes better understood by referring to the following detailed description when considered in conjunction with the

accompanying drawings in which like reference symbols indicate the same or the similar components, wherein:

Fig. 1 is a schematic view of opening pattern units formed at common and pixel electrodes in a prior art liquid crystal display;

Fig. 2 is a schematic cross sectional view of a liquid crystal display according to a preferred embodiment of the present invention;

Fig. 3A is a schematic view of opening pattern units formed at common and pixel electrodes according to one example of the present invention;

Fig. 3B is a schematic view of opening pattern units formed at common and pixel electrodes according to another example of the present invention;

Fig. 4A is a schematic view of an opening pattern of a pixel electrode according to the other example of the present invention;

Fig. 4B is a schematic view of an opening pattern of a common electrode according to the other example of the present invention;

Fig. 4C is a schematic view of the opening patterns of the pixel and common electrodes shown respectively in Figs. 4A and 4B in an overlapped state;

Fig. 5A is a schematic view of an opening pattern of a pixel electrode according to the other example of the present invention;

Fig. 5B is a schematic view of an opening pattern of a common electrode according to the other example of the present invention;

Fig. 5C is a schematic view of the opening patterns of the pixel and common electrodes shown respectively in Figs. 5A and 5B in an overlapped state;

Fig. 6A is a schematic view of an opening pattern of a pixel electrode according to the other example of the present invention;

Fig. 6B is a schematic view of an opening pattern of a common electrode according to the other example of the present invention;

5 Fig. 6C is a schematic view of the opening patterns of the pixel and common electrodes shown respectively in Figs. 6A and 6B in an overlapped state;

Fig. 7A is a schematic view of an opening pattern of a pixel electrode according to the other example of the present invention;

Fig. 7B is a schematic view of an opening pattern of a common electrode according to the other example of the present invention;

Fig. 7C is a schematic view of the opening patterns of the pixel and common electrodes shown respectively in Figs. 7A and 7B in an overlapped state;

Fig. 8A is a schematic view of an opening pattern of a pixel electrode according to the other example of the present invention;

Fig. 8B is a schematic view of an opening pattern of a common electrode according to the other example of the present invention;

Fig. 8C is a schematic view of the opening patterns of the pixel and common electrodes shown respectively in Figs. 8A and 8B in an overlapped state;

Fig. 9A is a schematic view of an opening pattern of a pixel electrode according to the other example of the present invention;

Fig. 9B is a schematic view of an opening pattern of a common electrode according to the other example of the present invention;

Fig. 9C is a schematic view of the opening patterns of the pixel and common electrodes shown respectively in Figs. 9A and 9B in an overlapped state;

Fig. 10A is a schematic view of an opening pattern of a pixel electrode according to the other example of the present invention;

Fig. 10B is a schematic view of an opening pattern of a common electrode according to the other example of the present invention;

Fig. 10C is a schematic view of the opening patterns of the pixel and common electrodes shown respectively in Figs. 10A and 10B in an overlapped state;

Fig. 11 are schematic views of various types of opening patterns for demonstrating the affect of opening pattern width and spacing on response speed and brightness;

Fig. 12A is a graph illustrating light transmissivity levels of test cells applying the opening patterns shown in Fig. 11;

Fig. 12B is a graph comparing the light transmissivity level of a test cell applying a specific opening pattern shown in Fig. 11 to the light transmissivity levels of test cells applying the other opening patterns shown in Fig. 11;

Fig. 13 is a graph illustrating response times as a function of gray scale of test cells applying the opening patterns shown in Fig. 11;

Fig. 14 is a graph illustrating response times as a function of gray scale of actual panels applying specific opening patterns shown in Fig. 11;

Figs. 15A to 15C are photographs of specific opening patterns shown in Fig. 11 at white gray scales;

Figs. 16A and 16B are photographs of specific opening patterns shown in Fig. 11 used to illustrate a change in partitioned regions according to a level of an applied voltage;

Figs. 17A and 17B are schematic views used to illustrate the change in intensity of a fringe field according to variations in opening pattern width;

Figs. 18A to 18D are schematic views illustrating orientation states of liquid crystal molecules at a peripheral portion of opening patterns;

Figs. 19 and 20 are schematic views of areas where texture occurs in specific opening patterns shown in Fig. 11;

Figs. 21A to 21C are schematic views of opening patterns where texture eliminating techniques have been applied.

Fig. 22 is a layout view of a TFT substrate according to the other example of the present invention.

Fig. 23 is a layout view of a color filter substrate opposite the TFT substrate in Fig. 22 according to the other example of the present invention.

Fig. 24 is a layout view of an LCD having the TFT substrate and the color filter substrate shown in Figs. 22 and 23 according to the other example of the present invention.

Fig. 25 is a sectional view of the LCD shown in Fig. 24 taken along the line XXV - XXV'.

Fig. 26 is a layout view of a color filter substrate according to the other example of the present invention.

Figs. 27 and 28 are layout views of LCDs according to the other example of the present invention respectively.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

Preferred embodiments of this invention will be explained with reference to the accompanying drawings.

Fig. 2 is a schematic cross sectional view of a liquid crystal display according to a preferred embodiment of the present invention.

As shown in Fig. 2, the liquid crystal display includes lower and upper substrates 10 and 20 arranged substantially in parallel with a predetermined gap therebetween. Liquid crystal material is injected between the lower and upper substrates 10 and 20 to form a liquid crystal layer. The liquid crystal material is comprised of liquid crystal molecules 30. A long axis of liquid crystal molecules 30 is oriented normal to the lower and upper substrates 10 and 20. Both the lower and upper substrates 10 and 20 are transparent material such as glass.

The lower substrate 10 is overlaid with a pixel electrode 12 that is connected to a switching element 11 to receive display signals. The pixel electrode 12 is formed of a transparent material such as indium tin oxide (ITO) or indium zinc oxide (IZO), and has an opening pattern (not shown). The switching element 11 is, for example, a thin film transistor, and is connected to a gate line (not shown), which transmits scanning signals, and a data line (not shown), which transmits picture signals. The switching element 11 turns the pixel electrode 12 on and off. A lower polarizer film 14 is attached to an outer surface of the lower substrate 10. In a reflection-type LCD, the pixel electrode 12 is

formed of non-transparent material. In this case, the lower polarizer film 14 is not necessary.

An inner surface of the upper substrate 20 is sequentially overlaid with a black matrix 21 that prevents the light leakage, a color filter 22, and a common electrode 23. The common electrode 23 is formed of a transparent material such as ITO or IZO, and has an opening pattern (not shown). A polarizer film 24 is attached to an outer surface of the upper substrate 20. Alternatively, the black matrix 21 or the color filter 22 may be formed on the lower substrate 10.

The LCD can be structured to operate in a normally black mode by arranging the lower polarizer film 14 and the upper polarizer film 24 so that the polarizing directions of each film are perpendicular to each other. It can also be structured to operate in a normally white mode by arranging the polarizing directions of each film to be parallel with each other. In the following description, only the arrangement for a normally black mode will be described. However, the invention can be also applied to the normally white mode. Fig. 3A shows a schematic view of opening patterns of the pixel and common electrodes 12 and 23 according to one example of the present invention. As shown in Fig. 3A, an opening pattern 101 of the pixel electrode 12 and an opening pattern 102 of the common electrode are respectively formed in a straight line. The opening pattern 101 is substantially parallel to the opening pattern 102. With this structure, the liquid crystal molecules 30 are arranged in parallel as a result of a fringe field generated by the opening patterns 101 and 102. Furthermore, the liquid crystal molecules 30 move into the parallel arrangement in a single step, thereby enabling a rapid response speed.

However, the above structure develops texture over a wide area of the screen. It is also possible that white after-images appear on the screen. When a screen displays a dark color on a bright background and then returns to the color of the bright background, it becomes brighter momentarily than the bright background. It is called as “white after-image”. Fig. 3B shows a schematic view of opening patterns of the pixel and common electrodes 12 and 23 according to another example of the present invention.

As shown in Fig. 3B, an opening pattern 111 of pixel electrode 12 and an opening pattern 112 of common electrode 23 are respectively formed in a curved shape. The ends of the opening patterns 111 and 112 are positioned in close proximity and their centers are bulging in opposite directions. However, this structure cannot arrange the liquid crystal molecules 30 in a single step, resulting in a slow response speed.

In the following examples, opening patterns of the pixel and common electrodes 12 and 23 will be described with reference to one pixel area. In a single pixel area, the pixel electrode 12 is substantially rectangular in shape having first and second long sides, respectively corresponding to left and right sides (in the drawings) of the pixel electrode 12. It has first and second short sides, respectively corresponding to top and bottom sides (in the drawings) of the pixel electrode 12. It also has a first corner formed by the ends of the first long side and the first short side, a second corner formed by the ends of the first short side and the second long side, a third corner formed by the ends of the second long side and the second short side, and a fourth corner formed by the ends of the first long side and the second short side. Further, the pixel electrode 12 includes an upper region and a lower region, the upper region corresponding to an upper half (in the

drawings) of the pixel electrode 12 and defined by the first long side, the second long side and the first short side, and the lower region corresponding to a lower half (in the drawings) of the pixel electrode 12 and defined by the first long side, the second long side and the second short side.

5 As the common electrode 23 is present over the entire surface of the upper substrate 20, a portion of the common electrode 23 roughly corresponding to the pixel electrode 12 in one pixel area will be described. Here, such portions of the common electrode 23 will be indicated by a dotted line and the identifying markers (i.e., upper region and lower region; first long side and second long side; first short side and second short side; and first, second, third and fourth corners) used for ease of explanation of the pixel electrode 12, which will also denote these portions of the common electrode 23 defined by the dotted lines.

Fig. 4A shows a schematic view of an opening pattern of the pixel electrode 12 according to the other example of the present invention.

As shown in Fig. 4A, a middle opening 121 is formed inwardly from the first long side where the upper and lower regions of the pixel electrode 12 meet. The middle opening 121 extends a predetermined distance toward the second long side while being tapered. The first long side is cut at a predetermined angle on both sides of the middle opening 121, forming a wide inlet area of the middle opening 121. Upper and lower openings 122 and 123 are formed in the upper and lower regions of the pixel electrode 12, respectively, proceeding from the second long side at a predetermined angle respectively toward the first and fourth corners of the pixel electrode 12 in a symmetrical manner.

Fig. 4B shows a schematic view of an opening pattern of the common electrode 23 according to the other example of the present invention.

As shown in Fig. 4B, the opening pattern of the common electrode 23 includes middle, upper and lower openings 210, 220 and 230 respectively spaced apart from the other at predetermined distances. The middle opening 210 includes a trunk 211 positioned where the upper and lower regions of the common electrode 23 meet and proceeding from the second long side a predetermined distance toward the first long side. First and second branches 212 and 214 are extended at a predetermined angle from the trunk 211 toward the first long side, and first and second sub-branches 213 and 215 extend along the first long side respectively from the first and second branches 212 and 214, toward the first and second short sides, respectively. The upper opening 220, which is formed in the upper region of the common electrode 23, includes a first body 221 that is formed extended from the second long side to the first short side at a predetermined distance from the second corner and parallel to the first branch 212. A first upper limb 222 extends from an end of the first body 221 along the first short side and until reaching the first long side, and a first lower limb 223 extends from an opposite end of the first body 221 along the second long side toward the second short side. The lower opening 230 includes a second body 231, a second lower limb 232, and a second upper limb 233. The lower opening 230 is arranged in the lower region and is symmetrical to the upper opening 220.

Fig. 4C shows a schematic view of the opening patterns of the pixel and common electrodes 12 and 23 shown respectively in Figs. 4A and 4B in an overlapped state.

As shown in Fig. 4C, the opening patterns of the pixel and common electrodes 12 and 23 divide the pixel electrode 12 into several regions. The openings 121, 122 and 123 of the pixel electrode 12 and the openings 210, 220 and 230 of the common electrode 23 are alternately arranged except where the trunk 211 of the middle opening 210 of the common electrode partially overlaps the middle opening 121 of the pixel electrode 12.

In this preferred embodiment, the lower and upper polarizer films 14 and 24 are arranged such that their polarizing directions are respectively 0° and 90° (or vice versa) with respect to the first and second short sides of the pixel electrode 12. With such an arrangement, when the liquid crystal molecules 30 are rearranged under the application of an electric field, they cannot be oriented in the polarizing direction of the polarizer films 14 and 24, without causing the texture problems. Furthermore, as the liquid crystal molecules 30 are fully oriented in parallel under the influence of the fringe field, the movement of the liquid crystal molecules 30 is completed in one step, resulting in a rapid response speed.

In addition, the opening portions of the pixel and common electrodes 12 and 23 are arranged generally in two directions normal to each other. Since the opening portions of the pixel and common electrodes 12 and 23 are alternately arranged, the fringe field is applied in four directions at one pixel area. Therefore, wide viewing angles can be obtained in all directions.

Fig. 5A shows a schematic view of an opening pattern of the pixel electrode 12 according to the other example of the present invention. As shown in Fig. 5A, the opening pattern of the pixel electrode 12 is a V-shaped opening 130. The V-shaped

opening 130 has a vertex in proximity to the second long side and positioned where the upper region meets the lower region of the pixel electrode 12, and opens toward the first long side of the pixel electrode 12. That is, an upper half 131 of the opening 130 extends at a predetermined angle from the vertex of the opening 130 toward the first long side of the pixel electrode 12 such that the upper half 131 is positioned wholly in the upper region of the pixel electrode 12, and a lower half 132 of the opening 130 extends at a predetermined angle from the vertex of the opening 130 toward the first long side of the pixel electrode 12 such that the lower half 132 is positioned wholly in the lower region of the pixel electrode 12. Further, the second and third corners of the pixel electrode 12 are cut away to form a curved shape.

Fig. 5B shows a schematic view of an opening pattern of the common electrode 23 according to the other example of the present invention.

As shown in Fig. 5B, the opening pattern of the common electrode 23 includes a right opening 240 and a left opening 250. The right opening 240 includes a base 241 formed along and extending past the first long side of the common electrode 23, and tapers from a middle portion along the first long side toward the first and second short sides. The base 241 of the right opening 240 also includes a projection 242 extending a predetermined distance from the base 241 toward the second long side and tapered in the same direction. A portion of the projection 242 adjacent to the base 241 is formed at a predetermined slant. The left opening 250 includes a body 251 formed along the second long side of the common electrode 23, an upper limb 252 extended at a predetermined angle from one end of the body 251 toward and continuing past the first corner of the common electrode 23, and a lower limb 253 extended at a predetermined angle from the

other end of the body 251 toward and continuing past the fourth corner of the common electrode 23. Centers of both the right and left openings 240 and 250 are positioned where the upper and lower regions of the common electrode 23 meet.

Fig. 5C shows a schematic view of the opening patterns of the pixel and common electrodes 12 and 23 shown respectively in Figs. 5A and 5B in an overlapped state.

As shown in Fig. 5C, the opening patterns of the pixel and common electrodes 12 and 23 divide the pixel electrode 12 into several regions. The V-shaped opening 130 of the pixel electrode 12 is placed between the right and left openings 240 and 250 of the common electrode 23. The upper and lower parts 131 and 132 of the V-shaped opening 130 proceed in parallel to the lower and upper limbs 252 and 253 of the left opening 250, respectively, as well as to the portion of the projection 242 adjacent to the base 241 of the right opening portion 240. An end of the projection 242 overlaps the vertex of the V-shaped opening portion 130. With the configuration of this example of the present invention as described above, the lower and upper polarizer films 14 and 24 are arranged such that their polarizing directions are the same as the previous example.

Fig. 6A shows a schematic view of an opening pattern of the pixel electrode 12 according to a fifth preferred embodiment of the present invention. As shown in Fig. 6A, the opening pattern of the pixel electrode 12 includes an upper opening 141 formed in the upper region of the pixel electrode 12, and a lower opening 142 formed in the lower region of the pixel electrode. If the pixel electrode 12 is divided into three areas of equal length, that is, first to third areas, with the first area having as its one side the first short side, the third area having as its one side the second short side, and the second area being

formed between the first and third areas, the upper opening 141 is positioned where the first and second areas meet, and the lower opening 142 is positioned where the second and third areas meet. The upper opening 141 extends from the first long side to the second long side of the pixel electrode 12 in the horizontal direction, and areas of the pixel electrode 12 corresponding to where the upper opening 141 is positioned along the first long side are cut away to form a curved shape. Similarly, the lower opening 142 extends from the second long side to the first long side of the pixel electrode 12 in the horizontal direction. Areas of the pixel electrode 12 corresponding to where the lower opening 142 is positioned along the second long side are cut away to form a curved shape. In addition, second and third corners of the pixel electrode 12 are cut such that they are rounded.

Fig. 6B shows a schematic view of an opening pattern of the common electrode 23 according to a fifth preferred embodiment of the present invention.

As shown in Fig. 6B, the opening pattern of the common electrode 23 is a zigzag-shaped opening 260. The zigzag-shaped opening 260 includes an upper part 261 proceeding from the first corner of the common electrode 23 at a predetermined slant toward and meeting the second long side of the common electrode 23. A middle part 262 extends at a predetermined slant from an end of the upper part 261 where the same meets the second long side toward and meeting the first long side of the common electrode 23. And a lower part 263 extends at a predetermined slant from an end of the middle part 262 where the same meets the first long side toward and meeting the third corner of the common electrode 23. If the common electrode 23 is divided into three areas of equal length, that is, first to third areas, with the first area having as its one side

the first short side, the third area having as its one side the second short side, and the second area being formed between the first and third areas, the upper and middle parts 261 and 262 converge where the first and second areas meet, and the middle and lower parts 262 and 263 converge where the second and third areas meet.

5 Fig. 6C shows a schematic view of the opening patterns of the pixel and common electrodes 12 and 23 shown respectively in Figs. 6A and 6B in an overlapped state.

As shown in Fig. 6C, the opening patterns of the pixel and common electrodes 12 and 23 divide the pixel electrode 12 into several regions. The portion where the upper and middle parts 261 and 262 of the opening 260 of the common electrode 23 meet overlaps an end of the upper opening portion 141 of the pixel electrode 12 adjacent to the second long side, and the portion where the middle and lower parts 262 and 263 of the opening 260 of the common electrode 23 meet overlaps an end of the lower opening portion 142 of the pixel electrode 12 adjacent to the first long side.

15 With the configuration of the example of the present invention as described above, the lower and upper polarizer films 14 and 24 are arranged such that their polarizing directions are the same as in the previous examples.

Fig. 7A shows a schematic view of an opening pattern of the pixel electrode 12 according to the other example of the present invention.

20 As shown in Fig. 7A, the opening pattern of the pixel electrode 12 includes an upper opening 151 formed parallel to the first and second short sides in the upper region of the pixel electrode 12, and a lower opening 152 also formed parallel to the first and second short sides in the lower region of the pixel electrode 12. If the pixel electrode 12

is divided into three areas of equal length, that is, first to third areas, with the first area having as its one side the first short side, the third area having as its one side the second short side, and the second area being formed between the first and third areas, the upper opening 151 is positioned where the first and second areas meet, and the lower opening portion 152 positioned where the second and third areas meet. The upper and lower openings 151 and 152 extend from a position in proximity to the first long side to a position in proximity to the second long side.

Fig. 7B shows a schematic view of an opening pattern of the common electrode 23 according to a sixth preferred embodiment of the present invention.

As shown in Fig. 7B, the opening pattern of the common electrode 23 includes first, second and third X-shaped openings 270, 280 and 290 spaced apart from each other at a predetermined distance along the length of the common electrode 23. A center area of each of the X-shaped openings 270, 280 or 290 is cut away to form an enlarged area approximately rectangular in shape. One line forming the “X” of the first X-shaped opening 270 extends from the first corner to the second long side of the common electrode 23 and its other line extends from the second corner to the first long side of the common electrode 23. Likewise, one line forming the “X” of the second X-shaped opening 280 extends from the first long side to the second long side of the common electrode 23 and its other line extends from the second long side to the first long side of the common electrode 23. In the same manner, one line forming the “X” of the third X-shaped opening 290 extends from the second long side to the fourth corner of the common electrode 23 and its other line extends from the first long side to the third corner of the common electrode 23.

Fig. 7C shows a schematic view of the opening patterns of the pixel and common electrodes 12 and 23 shown respectively in Figs. 7A and 7B in an overlapped state.

As shown in Fig. 7C, the opening patterns of the pixel and common electrodes 12 and 23 are alternately arranged, and divide the pixel electrode 12 into several regions.

With the configuration of this example of the present invention as described above, the lower and upper polarizer films 14 and 24 are arranged such that their polarizing directions are respectively 45° and 135° (or vice versa) with respect to the first and second short sides of the pixel electrode 12.

Fig. 8A shows a schematic view of an opening pattern of the pixel electrode 12 according to the other example of the present invention.

As shown in Fig. 8A, the opening pattern of the pixel electrode 12 includes an upper opening 160 formed in the upper region of the pixel electrode 12, and a lower opening 170 formed in the lower region of the pixel electrode 12. The upper opening 160 is T-shaped. That is, the upper opening 160 has a base 161 (the top of the "T") formed at a predetermined distance from where the upper and lower regions of the pixel electrode 12 meet. The base 161 extending from approximately the first long side to the second long side of the pixel electrode 12. The upper opening 160 also has a protrusion 162 extending substantially from a center of the base 161 in a direction toward the first short side of the pixel electrode 12, thereby bisecting the upper region of the pixel electrode 12 into left and right sub-areas. The lower opening 170 is formed parallel to the base 161 of the upper opening 160 and extends across the pixel electrode 12 approximately and at predetermined distances from the first long side to the second long

side of the pixel electrode 12 such that the lower opening 170 bisects the lower region of the pixel electrode 12 into upper and lower sub-areas.

Fig. 8B shows a schematic view of an opening pattern of the common electrode 23 according to the other example of the present invention.

5 As shown in Fig. 8B, the opening pattern of the common electrode 23 includes two upper openings 310 and 320, a middle opening 330, and a lower opening 340. The two upper openings 310 and 320 are spaced apart from each other at a predetermined distance in the upper region of the common electrode 23, and are parallel to each other as well as to the first and second long sides of the common electrode 23. The middle and lower openings 330 and 340 are spaced apart from each other at a predetermined distance in the lower region of the common electrode 23, and are parallel to each other and to the first and second short sides of the common electrode 23. Both end portions of the middle and lower openings 330 and 340 are enlarged in roughly a triangular shape, and the triangle-shaped end portions of the middle and lower opening portions 330 and 340 proceed over the first and second long sides of the common electrode 23.

Fig. 8C shows a schematic view of the opening patterns of the pixel and common electrodes 12 and 23 shown respectively in Figs. 8A and 8B in an overlapped state.

As shown in Fig. 8C, the opening patterns of the pixel and common electrodes 12 and 23 divide the pixel electrode 12 into several regions. That is, ends of the upper opening portions 310 and 320 of the common electrode 23 farthest from the first short side of the common electrode 23 overlap the base 161 of the T-shaped opening 160 of the pixel electrode 12. Accordingly, the upper openings 310 and 320 of the common

electrode 23, and the protrusion 162 of the T-shaped opening 160 of the pixel electrode 12 divide an area of the pixel electrode 12 defined by the base 161 of the T-shaped opening 160, the first and second long sides of the pixel electrode 12, and the first short side of the pixel electrode 12 into four sub-areas. The middle and lower openings 330 and 340 of the common electrode 23, and the lower opening 170 of the pixel electrode 12 divide an area of the pixel electrode 12 defined by the base 161 of the T-shaped opening 160, the first and second long sides of the pixel electrode 12, and the second short side of the pixel electrode 12 into four sub-areas.

With the configuration of this example of the present invention as described above, the lower and upper polarizer films 14 and 24 are arranged such that their polarizing directions are the same as in the immediate previous example. Accordingly, the orienting direction of the liquid crystal molecules 30 becomes 45° with respect to the polarizing direction of the polarizer films 14 and 24 so that the response speed is rapid and the texture is decreased, resulting in enhanced picture quality. The opening portions of the pixel and common electrodes 12 and 23 proceed generally in two directions normal to each other. Furthermore, as the opening portions of the pixel and common electrodes 12 and 23 are alternately arranged, the fringe field in one pixel area is applied in all directions.

Fig. 9A shows a schematic view of an opening pattern of the pixel electrode 12 according to the other example of the present invention. As shown in Fig. 9A, the opening pattern of the pixel electrode 12 is a single linear opening 180 parallel to the first and second short sides of the pixel electrode 12. If the pixel electrode 12 is divided into three areas of equal length, that is, first to third areas, with the first area having as its one

side the first short side, the third area having as its one side the second short side, and the second area being formed between the first and third areas, the linear opening 180 is positioned where the second and third areas meet.

Fig. 9B shows a schematic view of an opening pattern of the common electrode 23 according to the other example of the present invention.

As shown in Fig. 9B, the opening pattern of the common electrode 23 includes an upper opening 350 formed in the upper region of the common electrode 23 and a lower opening 360 formed in the lower region of the common electrode. The upper opening 350 includes a base 351, a trunk 352, and two branches 353 and 354. The base 351 of the upper opening 350 is formed roughly in a triangular shape and positioned extending over and past the first short side of the common electrode 23. The trunk 352 is linearly extended from an apex of the base 351 in a direction toward the second short side of the common electrode 23. The branches 353 and 354 are branched from a distal end of the trunk 352 toward and extending over the first and second long sides of the common electrode 23, each of the branches 353 and 354 forming an obtuse angle with respect to the trunk 352. The lower opening 360 linearly proceeds in a direction parallel to the first and short sides of the common electrode 23. Both ends of the lower opening 360 are enlarged in roughly a triangular shape and extend over the first and second long sides of the common electrode 23.

Fig. 9C shows a schematic view of the pixel and common electrodes 12 and 23 shown respectively in Figs. 9A and 9B in an overlapped state.

As shown in Fig. 9C, the branches 353 and 354 of the upper opening 350 of the common electrode 23 roughly divide the pixel electrode 12 into upper and lower areas.

The trunk 352 of the upper opening 350 of the common electrode 23 bisects the upper area of the pixel electrode 12 into two sub-areas, one sub-area having as its one side the second long side of the pixel electrode 12 and the other sub-area having as its one side the first long side of the pixel electrode 12. The lower opening 360 of the common electrode 23, and the linear opening 180 of the pixel electrode 12 trisect the lower area of the pixel electrode 12 into upper, middle and lower sub-areas.

With the configuration of this example of the present invention as described above, the lower and upper polarizer films 14 and 24 are arranged such that their polarizing directions are the same as in the immediate previous example. With this structure, effects similar to those obtained in the previous example are realized.

Fig. 10A shows a schematic view of an opening pattern of the pixel electrode 12 according to the other example of the present invention.

As shown in Fig. 10A, the pixel electrode 12 is formed of four oval-shaped portions sequentially interconnected in the longitudinal direction.

Fig. 10B shows a schematic view of an opening pattern of the common electrode 23 according to the other example of the present invention.

As shown in Fig. 10B, the opening pattern of the common electrode 23 includes four diamond-shaped openings 370, and left and right openings 380 and 390 surrounding the diamond-shaped openings 370. The diamond-shaped openings 370 are arranged over a longitudinal center of the common electrode 23 and are spaced apart from each other at a predetermined distance. Inner sides of the left and right openings 380 and 390 facing the diamond-shaped openings 370 substantially form cycloids such that four partial ovals result, each oval surrounding one of the diamond-shaped openings 370.

Fig. 10C shows a schematic view of the pixel electrode 12 and the common electrode 23 shown respectively in Figs. 10A and 10B in an overlapped state. As shown in Fig. 10C, each diamond-shaped opening 370 of the common electrode 23 is placed at the center of the corresponding oval-shaped portion of the pixel electrode 12. Also, the left and right openings 380 and 390 of the common electrode 23 surround the pixel electrode 12 at a predetermined distance.

With the configuration of this example of the present invention as described above, the lower and upper polarizer films 14 and 24 are arranged such that their polarizing directions are respectively 0° and 90° (or vice versa) with respect to the first and second short sides of the pixel electrode 12. The opening patterns of the pixel and common electrodes 12 and 23 in these example were designed to satisfy the following conditions to the most, the conditions being particular to opening patterns of the type for obtaining the partitioned orientation of the liquid crystal molecules 30.

First, in order to obtain a maximum viewing angle, it is preferable that one pixel area has four partitioned regions for orienting the liquid crystal molecules 30.

Second, to obtain a stable partitioned orientation, disinclination or texture should be eliminated or minimized outside of the partitioned regions. Disinclination occurs when the long axes of liquid crystal molecules are oriented in various directions in a confined area, particularly when the long axes are inclined toward one another. Therefore, it is preferable that the opening patterns of the pixel and common electrodes 12 and 23 are alternately arranged, and the end portions of the opening patterns are adjacent to each other. That is, when viewed from above, the opening patterns of the pixel and common electrodes 12 and 23 are preferably structured in the form of closed

polygons. Furthermore, since disinclination is prone to occur when the opening patterns are structured having acute angles, it is preferable that the opening patterns are formed to have only obtuse angles. A stable partitioned orientation of liquid crystal molecules also enhances brightness. At areas where the orientation of the liquid crystal molecules is dispersed, lights tend to leak at an off state, and dark portions are generated at an on state. Also, this dispersion of the orientation of liquid crystal molecules generates after-images when the liquid crystal molecules are rearranged.

Third, in order to obtain a high level of brightness, the following conditions should be satisfied. The angle made by the two directors of the liquid crystal molecules at adjacent partitioned regions is preferably about 90° . The directors arranged at this angle minimizes the disinclination. The best brightness can be obtained when the angle between the light transmission axis of the polarizer film and the director for liquid crystal molecules is 45° . It is preferable that twisting or bending of the opening patterns of the pixel and common electrodes 12 and 23 is minimized.

Finally, in order to obtain a rapid response speed of the liquid crystal molecules 30, it is preferable again that the opening patterns of the pixel and common electrodes 12 and 23 are neither twisted nor bent too much. That is, it is preferable that the opening patterns of the pixel and common electrodes 12 and 23 linearly face each other.

The effect of an opening width of the opening patterns and a spacing interval between the openings on light transmission and response speed will now be described.

In order to investigate such an interrelation, nine panels, each with different opening patterns were made and tested.

Fig. 11 shows schematic views of nine different opening patterns A -- J for demonstrating the effect of opening pattern width and spacing on response speed and brightness. In the drawing, the opening patterns of the common electrode are indicated by hatched lines, and the opening patterns of the pixel electrode are indicated by solid lines.

As shown in Fig. 11, the B, C and D opening patterns are identical and the E, F and G opening patterns are identical. However, these opening patterns differ in opening width and spacing. The I and J opening patterns differ in the number of openings used, effectively having different opening spacings. The A opening pattern has a shape similar to that of the B, C and D opening patterns except for the formation at a center area of the A opening pattern. As a result, the A opening pattern is different in opening spacing from the B, C and D opening patterns. The opening width and the opening spacing of each opening pattern are listed in Table 1.

TABLE 1

	Opening Width (μm)	Opening Spacing (μm)
A	10	33.5
B	10	22.5
C	7	25.5
D	13	19.6
E		24
F		21
G		27
I	10	Narrow Spacing:29 Wide Spacing:32
J	10	Narrow Spacing:10 Wide Spacing:16

Fig. 12A is a graph illustrating light transmissivity levels of test cells applying the A through J opening patterns, and Fig. 12B is a graph comparing the light transmissivity level of a test cell applying the B opening pattern to the light transmissivity levels of test cells applying the A through J opening patterns. As shown in the graphs, the light transmissivity level of the test cell applying the G opening pattern is the highest, exceeding 13%. The ranking of the light transmissivity levels of the test cells from highest to lowest according to which opening pattern is used is G, E, I, B, D, A, C, F, and J in order.

Fig. 13 is a graph illustrating response times as a function of gray scale of test cells applying the A through J opening patterns. Although only sixty-nine (69) gray scales are used in an actual application, the experiment was performed with one hundred and ten (110) gray scales. As shown in the graph, response times of the test cells applying the B, C, D, and J opening patterns were relatively fast over the whole range of gray scales. For the test cells applying the other opening patterns, the response times were relatively slow. In the case of the test cells applying the A and I opening patterns, the slow response times were due to the movement of texture. In the case of the test cells applying the E, F, and G opening patterns, the slow response times can be attributed to the two-step movement of liquid crystal molecules.

The A through J opening patterns shown in Fig. 11 were applied to actual panels and the panels were tested. Testing was performed on a total of four panels for each opening pattern. The results are listed in Table 2.

The results of the experiment performed with the actual panels were similar to the results when using the test cells. However, there were some differences as follows. First, the actual panel of the I opening pattern exhibited a higher response speed than the test cell of the same opening pattern. Also, better results with regard to brightness were obtained with the actual panel of the J opening pattern than when the test cell was used. Specifically, the brightness of the test cell applying the J opening pattern was 75% of the cell applying the B opening pattern, whereas this was increased to 90% when the J opening pattern was applied to the actual panel.

When the actual panels were used, white after-images were generated with the application of the C, I, and J opening patterns. The white after-image appeared too much with the application of the C opening pattern that picture quality was impaired beyond the tolerance. However, the generation of white after-images was low enough when the I and J opening patterns were applied so that with some improvement, the panels could be used.

On the basis of the above results, the opening patterns are to be selected depending on what the intended area of improvement is. If the improvement of brightness and the minimization of white afterimages are desired, it is preferable to use the B, D, E, and I opening patterns. However, if an improvement in response speed while keeping the brightness at a normal level is desired, the B, D, and I opening patterns are preferred. Finally, if what is needed is solely an improvement in response speed (without concerning brightness), the D and J opening patterns are preferred.

In order to further examine the interrelation between the response speed and the opening width of the opening patterns, the differences in the optical characteristics of

panels applying the B, C, and D opening patterns, which have the same shape but different opening widths, will now be described. Fig. 14 is a graph illustrating response times as a function of gray scale of actual panels applying the B, C, and D opening patterns. As shown in the graph, the response times of the panels applying the opening patterns exhibited the following relation (based on the type of opening pattern) when 20 to 40 gray scales were used: $D < B < C$. It is evident, therefore, that the larger the width of the opening pattern the faster the response time.

Roughly between 40 and 45 gray scales, the response time of the panel applying the C opening pattern is shorter than that of the panel applying B opening pattern, and after 45 gray scales, the response time of the panel applying the C opening pattern is shorter than that of the panel applying the D opening pattern. However, such a change in the response time of the panel applying the C opening pattern is not actually taking place, but instead is given the appearance of change as a result of the generation of white after-images. That is, the response waveform is distorted due to the white after-images so that the response time seems to be shorter than it actually is. Accordingly, the conclusion originally reached that the larger the width of the opening pattern the faster the response speed remains valid.

With the use 60 gray scales or more, the response speed slows considerably due to the occurrence of texture. In conclusion, the panel applying the D opening pattern, which has the greatest width, exhibits the most stable characteristics. Figs. 15A to 15C are photographs of the C, B and D opening patterns, respectively, at white gray scales. As seen from the photographs, the C opening pattern with poor texture stability displays the lowest level of brightness, with the B and D opening patterns exhibiting similarly

higher levels of brightness. The D opening pattern exhibits a low opening ratio due to its significant width, but displays good texture stability such that panels applying this opening pattern have a high brightness. Texture stability is determined by the intensity of the fringe field and the width of the opening pattern.

5 The boundary areas between adjacent partitioned regions in the C, B and D opening patterns are formed differently. That is, two clearly distinguishable textures are present in most of the boundary areas of the C opening pattern, and with the B opening pattern, the boundary areas are again distinguishable but not as clearly as with the C opening pattern. The boundary areas of the D opening pattern, on the other hand, are not clearly formed and are faint in many portions.

10 Figs. 16A and 16B are photographs of the C and D opening patterns applied to test cells in which a change in the partitioned regions according to a level of an applied voltage is shown.

15 In the C opening pattern, two clearly distinguishable textures are present in the boundary areas when the applied voltage reaches 3.5V, and becomes clearer with further increases in the applied voltage. However, in the D opening pattern, the boundary areas are somewhat clearly distinguishable only when the applied voltage reaches 5V. Such distinguishable boundary areas are a result of the non-uniform orientation of the liquid crystal molecules. To better describe such a phenomenon, the intensity of the fringe field as a function of the widths of the opening patterns will be examined.

20 Figs. 17A and 17B are schematic views used to illustrate the changes in the intensity of a fringe field according to variations in opening pattern width. As the width of the opening pattern becomes larger, the horizontal component of the fringe field

experiences corresponding increases. The horizontal component of the fringe field plays an important role in determining the orienting direction of liquid crystal molecules. Therefore, opening patterns with a large width are preferred in forming partitioned regions. In contrast, the larger the width of the opening pattern the weaker the intensity of the vertical component of the electric field working at the center of the opening pattern.

Figs. 18A to 18D are schematic views illustrating orienting states of liquid crystal molecules at a peripheral portion of the opening patterns. When the width of the opening pattern is relatively small, the liquid crystal molecules are horizontally oriented to some degree even at the center area of the opening pattern. That is, they are slightly inclined when the applied voltage is low, but completely oriented in the horizontal direction when the applied voltage is high. This is due to the vertical component of the electric field being strong even at the center area of the opening pattern. As a result, the light tends to leak and the boundary area between the partitioned regions is formed by two separate lines. Furthermore, when the orienting direction of the liquid crystal molecules is changed by 180° , elasticity becomes greater due to the small width of the opening pattern. In contrast, as the horizontal component of the fringe field is weak, the fringe field is not strong enough to overcome the elasticity, thereby resulting in the orienting direction of the liquid crystal molecules at the boundary areas becoming non-uniform between the partitioned regions. Such a non-uniform orientation of the liquid crystal molecules occurs even in micro regions of the pixel.

When the width of the opening pattern is relatively large, the long axes of the liquid crystal molecules are perpendicular to the electrodes at the center area of the

opening pattern. As the applied voltage is increased, the liquid crystal molecules are slightly inclined, but the degree of inclination is less than when the opening pattern has a small width. Therefore, only a minimal amount of light leaks and the boundary area between adjacent partitioned regions is shaped with a dark line.

5 As described previously, the greater the width of the opening pattern the more rapid the response speed, and as stated above, a greater width of the opening pattern leads to more uniform micro regions of the pixel. When the width of the opening pattern is great, although the opening ratio is low, the orientation of the liquid crystal molecules is uniform enough to obtain a satisfactory degree of brightness. According to the above experimental results, it is preferable that the opening width of the opening pattern is in the range of $13 \pm 3 \mu\text{m}$, and the cell gap is in the range of about $4\text{-}6 \mu\text{m}$.

The effect of opening spacing on the optical characteristics of the opening patterns will now be described.

15 The I and J opening patterns have the same total widths but effectively different spacings. According to the experimental results with respect to the test cells, the optical characteristics of the I and J opening patterns are significantly different. However, when actual panels apply these opening patterns, the resulting optical characteristics of the I and J opening patterns do not vary by such a degree. It is viewed that this is a result of the such factors as the difference in the type of alignment layer used, whether a protective insulating layer is used, the difference in the waveforms of the applied voltage, etc. However, when the speeds of moving picture images are compared in the actual panels, they are more rapid with the J opening pattern than with the I opening pattern. This can be easily demonstrated by observing the motion of a dark rectangle on a gray

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background. The only difference in response speed occurs by variations in the gray scales.

Regarding the opening width of the opening pattern, when the spacing between the opening portions of the opening pattern becomes smaller, the opening ratio is significantly reduced but the brightness does not change much. This is due to texture. That is, when the distance between the opening portions is increased, it becomes difficult to control the texture, whereas it can be easily controlled when the opening spacing is small. Therefore, when the distance between the opening portions is small, the opening ratio is reduced but it becomes easy to control the texture which compensates the brightness. The exception is the I opening pattern, in which even though the distance between the opening portions is large, a high brightness can be achieved because texture is easily controlled.

In brief, a smaller distance between the opening portions results in an improvement of the response speed at various gray scales. Even though the brightness is negatively affected due to the decreasing opening ratio, this can be compensated for to some degree by controlling texture.

There exists a direct correlation between texture and response speed. Moving texture reduces response speed. When a high voltage is applied, the response speed is reduced in most of the opening patterns. This is due to the generation of texture. Therefore, if texture can be properly controlled, picture quality as well as response speed can be improved. Techniques of preventing texture will now be described.

Figs. 19 and 20 show schematic views of portions where texture is generated in the B and J opening patterns, respectively. The opening pattern shown in Fig. 19 is

nearly identical to that shown in Fig. 4C. However, in the opening pattern of Fig. 19, second and third openings 122 and 123 of the pixel electrode 12 begin from the first long side of the pixel electrode 12 and extend toward the second long side of the pixel electrode 12 nearly reaching the same, whereas in the opening pattern of Fig. 4C, the second and third openings 122 and 123 are structured in the opposite manner. Furthermore, portions of the second long side of the pixel electrode 12 adjacent to ends of the second and third openings 122 and 123 of the opening pattern of Fig. 19 are protruded externally to prevent the interconnection of the partitioned regions of the pixel electrode 12 from deteriorating due to the opening portions 122 and 123.

Portions where texture occurs mainly correspond to areas where ends of the opening portions of the common electrode 23 and ends of the opening portions of the pixel electrode 12 meet. When the upper and lower substrates are appropriately arranged, the occurrence of texture is low, whereas when the substrates are inappropriately arranged, half moon-shaped textures, which do not cause the generation of white after-images, occur. In order to inhibit such texture occurrence, the width of the ends of the opening portions of the common electrode 23 may be enlarged. Through such enlargement, the tolerance of error in arrangement can be increased.

The opening pattern shown in Fig. 20 is similar to that shown in Fig. 8C, but differs in the number of openings extending across the pixel electrode from the first long side to the second long side. Furthermore, the openings of the pixel electrode 12 are such that they are open where they begin at the first long side of the pixel electrode 12 and extend across toward, but not reaching, the second long side of the pixel electrode

12. Portions of the second long side of the pixel electrode 12 adjacent to ends of these openings are protruded externally.

The occurrence of texture is concentrated at areas “a” corresponding to ends of openings of the common electrode 23 proceeding across from the first long side to the second long side of the common electrode 23. Furthermore, texture occurs also along the second short side of the pixel electrode 12, or area “b”, which is deformed outwardly to enable a connection with the source electrode, as well as at area “c” at an end of an opening of the pixel electrode 12.

Such texture can be inhibited in the following way. In the case of area a, a width of the ends of the openings of the common electrode 23 are increased. In the case of area b, the openings of the common electrode 23 are structured to overlap part of area b. For this purpose, it is necessary to control the width and spacing of the opening portions. When the spacing is decreased, the opening ratio is reduced but the response speed is enhanced. In the case of area c, the end of the opening of the pixel electrode 12 extended from the first short side is formed having sharp edges.

Figs. 21A to 21C illustrate opening patterns where the above-described texture eliminating techniques have been applied.

In the above description, a structure in which the opening patterns are formed at both the pixel and common electrodes 12 and 23 is disclosed. However, it is also possible to form the opening patterns, together with the protrusions, only at the pixel electrode 12. In this case, the protrusions are formed using a gate insulating layer or a protective layer. In the formation of the protrusions, care should be taken to avoid the

formation of parasitic capacitance between electrical lines. The openings and the protrusions can be arranged as illustrated in Fig. 21.

Alternatively, the opening patterns may be formed only in the pixel electrode 12 while forming the protrusions in the common electrode 23. In this case, the openings and the protrusions can be arranged also as illustrated in Fig. 21.

As described above, the inventive liquid crystal display obtains a wide viewing angle, and exhibits stable orientation of the liquid crystal molecules and a rapid response speed.

Figs. 22 and 23 are layout views of a TFT substrate and a color filter substrate according to the other examples respectively.

As shown in Fig. 22, a portion 210 of a gate line 21 which transmits a scanning signal is formed to have a trapezoidal shape without the lower side. Then, the portion 210 made of opaque metal blocks the light from the backlight, and, therefore the light leakage or the decrease of luminance can be prevented.

Next, as shown in Fig. 23, a black matrix 11 is formed on the color filter substrate to cover the regions where disclination is generated and the aperture in the common electrode. The disclination regions are, as described above, the region where the aperture 27 on the TFT substrate meets the boundary of the pixel electrode 20 and the region where the saw-shaped apertures 17 and 27 are bent. The black matrix pattern which covers the disclination includes, as shown in Fig. 23, an edge portion 111 surrounding and defining a pixel region, a saw-shaped portion 112 to cover the apertures 17, a triangular portion 113 to cover the disclination between saw-shaped apertures 17 and 27 and a center portion 114 put across the pixel region to cover the disclination

generated in the bent portion of the apertures 17 and 27. Then, the light leakage generated by the disclination or the apertures is prevented by the black matrix 11. Moreover, the aperture ratio does not decrease additionally though a relatively large area of black matrix 11 is formed, because the region that the black matrix covers may not be used for display.

Fig. 24 is a layout view of an LCD according to the other example of the present invention. Fig. 25 is a sectional view of an LCD shown in Fig. 24 taken along the line XXV - XXV'.

As shown in Figs. 24 and 25, a portion 210 of a gate line 21 is formed on a lower TFT substrate. The gate line has a trapezoidal shape without the lower side. An insulating layer 22 covers the gate line 21. A pixel electrode 23 is formed on the insulating layer 22, and portions of the pixel electrode 23 are removed to form saw-shaped apertures 27 over the portion 210 of the gate line 21. A vertical alignment layer 24 is formed on the pixel electrode 20.

On the other hand, a black matrix 11 is formed on an upper color filter substrate to shield the outside of the pixel regions, the aperture and the disclination regions. In the pixel region within the black matrix 11, a color filter 12 is formed. A passivation layer 15 is formed on the black matrix 11 and the color filter 12. An ITO common electrode 13 is formed thereon and patterned to remove the portion overlapping the black matrix 11. The aperture 17 formed on the upper substrate is arranged alternately to the aperture 27 formed on the lower substrate, and the apertures 17 and 27 are parallel to each other.

A liquid crystal material layer with negative dielectric anisotropy is interposed between two substrate 100 and 200, and the liquid crystal molecules are homeotropically

aligned to the substrates 100 and 200 by the aligning force of the alignment layers 14 and 24.

It is possible to form a gate line as in a conventional LCD and the apertures formed on the lower substrate is also covered by the black matrix, as shown in Fig. 26 which is a layout view of a color filter substrate according to the other example of the present invention.

A black matrix 11 is formed to define a pixel region and to cover the aperture 17 to form multi-domain, the disclination between saw-shaped apertures 17 and 27 and the disclination generated in the bent portion of the apertures 17 and 27 as in the immediate previous example. In addition, the black matrix 11 includes another portion to cover the aperture 27 formed on the lower substrate.

If the black matrix covers the apertures and the disclination as in this example, it is not necessary to consider the influence by the gate line pattern and no additional process step is required.

Moreover, the shape of the pixel electrode may be changed instead of forming the branch aperture in some of the previous examples.

As shown in the above, the region where the disclination is generated is the region where the aperture on the TFT substrate meets the boundary of the pixel electrode. This region is the place where the first condition that the bent angle of the aperture pattern should be an obtuse angle is not satisfied because the boundary of the pixel electrode is essentially similar to the aperture. In other words, the liquid crystal molecules do not arrange in order and such arrangement causes the decrease of the luminance and after-image.

Therefore, in the other example of the present invention, the shape of the pixel electrode 21 is changed to make an angle between the aperture 27 formed in the pixel electrode 21 and the boundary of the pixel electrode 21 to be an obtuse angle. Then, as shown in Fig. 27, the pixel electrode 21 has a saw shape that is convex between the apertures 17 and 27 formed in the common electrode and the pixel electrode respectively.

In the other example of the present invention as shown in Fig. 28, the pixel electrode is formed to have a saw shape surrounding the apertures.

Since the pixel electrode 22 is formed to have a saw shape surrounding the apertures 17 and 27, the regions where the apertures 17 and 27 meet the boundary of the pixel electrode are removed thereby removing the disclination.

According to the embodiments of the present invention, multi-domain LCDs are formed using various ITO pattern to control the arrangement of liquid crystal molecules, therefore wide viewing angle is obtained, disclination is removed and the luminance is increased.

In the described embodiments of the present invention, only apertures form the domains. However, the domains may be formed by protrusions along with apertures. In this case, the protrusions may be made of a gate insulating layer and/or a passivation layer. The layout of the protrusions and the aperture pattern may be the same as that of the apertures in Figs. 21A to 21C. The protrusions may be formed on the color filter substrate.

While the present invention has been described in detail with reference to the preferred embodiments, those skilled in the art will appreciate that various modifications

and substitutions can be made thereto without departing from the spirit and scope of the present invention as set forth in the appended claims.

WHAT IS CLAIMED IS:

1. A liquid crystal display, comprising:

a first insulating substrate having a top surface and a bottom surface;

a pixel electrode formed on the top surface of the first insulating substrate, the

5 pixel electrode having a first opening pattern;

a second insulating substrate having a top surface and a bottom surface;

a common electrode formed on the bottom surface of the second insulating substrate, the common electrode having a second opening pattern; and

a liquid crystal layer sandwiched between the first substrate and the second substrates,

wherein the first opening pattern and the second opening patterns each have a middle linear portion, the linear portions being alternately arranged parallel to each other.

2. The liquid crystal display of claim 1, wherein the first opening pattern comprises a first opening formed in an upper region of the pixel electrode in a first direction and a second opening formed in a lower region of the pixel electrode in a second direction normal to the first direction, and the second opening pattern comprises a first trunk opening formed in a first region of the common electrode in the first direction and a second trunk opening formed in the second region of the common electrode in a second direction wherein the first region and the second region of the common electrode respectively correspond to the upper region and the lower region of the pixel electrode.

3. The liquid crystal display of claim 2, wherein the first direction is at a slant with respect to long sides or short sides of the pixel electrode.

4. The liquid crystal display of claim 3, wherein the second opening pattern further comprises first branch openings overlapping a first short side and a second short side of the pixel electrode and second branch openings overlapping a first long side and a second long side of the pixel electrode, and the first opening pattern further comprises a third opening formed where the upper and lower regions of the pixel electrode meet while proceeding parallel to the first short side and the second short side of the pixel electrode, the first opening pattern and the second opening patterns partitioning the pixel electrode into a plurality of sub-regions each shaped as a closed polygon.

5. The liquid crystal display of claim 4, wherein each of the second branch openings has a width greater than a width of the trunk opening portion.

6. The liquid crystal display of claim 2, wherein the first direction is parallel to one of the long side and the short side of the pixel electrode.

7. The liquid crystal display of claim 6, wherein each of the first trunk opening and the second trunk opening has opposite ends, each end having a gradually enlarged width.

8. The liquid crystal display of claim 6, wherein one of the second trunk opening overlaps the second short side of the pixel electrode.

9. The liquid crystal display of claim 6, wherein the first opening portion has opposite ends, each end having a gradually reduced width.

10. The liquid crystal display of claim 2, further comprising a first polarizer film formed on the bottom surface of the first substrate, and a second polarizer film formed on the top surface of the second substrate, the first polarizer film and the second

polarizer film being arranged to have polarizing directions of 45° with respect to the first direction and the second direction, respectively.

11. The liquid crystal display of claim 2, wherein the pixel electrode has protrusions at portions adjacent to the ends of the first opening and the second opening.

5 12. The liquid crystal display of claim 1, wherein the opening width of the first opening pattern and the second opening pattern is in the range of $10\text{-}16\mu\text{m}$

13. A liquid crystal display, comprising:
a first insulating substrate having a top surface and a bottom surface;
a pixel electrode formed on the top surface of the first insulating substrate, and
the pixel electrode having a first opening pattern;

a second insulating substrate having a top surface and a bottom surface;
a common electrode formed on the bottom surface of the second insulating
substrate, the common electrode having a second opening pattern;

a liquid crystal layer sandwiched between the first substrate and the second
substrate,

wherein the first opening pattern and the second opening pattern overlap each
other to thereby partition the pixel electrode into a plurality of sub-regions, each sub-
region being polygonal in shape with two longest sides proceeding parallel to each other.

14. The liquid crystal display of claim 13, wherein the sub-regions of the
20 pixel electrode are classified into a first type that has the longest sides arranged in a first
direction, and a second type that has the longest sides arranged in a second direction
normal to the first direction.

15. The liquid crystal display of claim 14, wherein the first direction is at a slant with respect to long or short sides of the pixel electrode.

16. The liquid crystal display of claim 14, wherein the first direction is parallel to one of long and short sides of the pixel electrode.

17. The liquid crystal display of claim 13, wherein the opening width of the first opening pattern and the second opening pattern is in the range of 10-16 μ m

18. A liquid crystal display, comprising:
a first insulating substrate having a top surface and a bottom surface;
a pixel electrode formed on the top surface of the first insulating substrate, the pixel electrode having a first opening pattern;
a second insulating substrate having a top surface and a bottom surface;
a common electrode formed on the bottom surface of the second insulating substrate, the common electrode having a second opening pattern; and
a liquid crystal layer sandwiched between the first substrate and the second substrate while contacting the pixel electrode and the common electrode,

wherein the first opening pattern and the second opening pattern generate fringe fields when voltage is applied between the pixel electrode and the common electrode, and the orienting direction of the liquid crystal molecules due to fringe fields corresponds to the orienting direction of the liquid crystal molecules resulting from a force exerted by molecules.

19. The liquid crystal display of claim 18, wherein the liquid crystal molecules are oriented in four directions due to the fringe fields.

20. The liquid crystal display of claim 18, wherein the opening width of the first opening pattern and the second opening pattern is in the range of 10-16 μ m

21. A liquid crystal display, comprising:

a first insulating substrate having a top surface and a bottom surface;

5 a pixel electrode formed on the top surface of the first insulating substrate, the pixel electrode having a first opening pattern at each pixel area, the pixel electrode with the first opening pattern being substantially rectangular in shape in each pixel area and having (a) a first long side and a second long side, (b) a first short side and a second short side, (c) an upper region corresponding to roughly half of the pixel electrode defined by the first long side and the second long side and the first short side, and (d) a lower region corresponding to roughly half of the pixel electrode defined by the first long side and the second long side and the second short side;

a second insulating substrate having a top surface and a bottom surface;

a common electrode formed on the bottom surface of the second insulating substrate, the common electrode having a second opening pattern at each pixel area, the common electrode with the second opening pattern being substantially rectangular in shape in each pixel area and having (a) a third long side and a fourth long side, (b) a third short side and a fourth short side, (c) an upper region corresponding to roughly half of the common electrode defined by the third long side and the fourth long side and the third short side, and (d) a lower region corresponding to roughly half of the pixel electrode defined by the third long side and the fourth long side and the fourth short side; and

20

a liquid crystal layer sandwiched between the first substrate and the second substrate while contacting the pixel electrode and the common electrode,

wherein the first opening pattern comprises a first opening proceeding from the first long side in a direction toward the second long side, second and third openings proceeding from the second long side at a predetermined incline toward the first long side, the second and third openings being symmetrical with respect to the first opening,

wherein the second opening pattern comprises fourth to sixth openings, the fourth opening comprising a trunk proceeding in a direction horizontal to the first and second short sides, first and second branches branched from the trunk toward the first long side slightly up and downwards, and first and second sub-branches respectively extended from the first and second branches opposite each other in a direction parallel to the first and second long sides, the fifth opening portion comprising a body parallel to the first branch, and first and second limbs extended from top and bottom ends of the body respectively in directions parallel and perpendicular to the first and second short sides, the sixth opening portion being symmetrical to the fifth opening portion with respect to the fourth opening portion, and wherein the first and second opening patterns are alternately arranged when viewed from above.

22. The liquid crystal display of claim 21, wherein the first and second sub-branches, and the second limbs have a width greater than a width of other portions of the second opening pattern.

23. The liquid crystal display of claim 21, further comprising a first polarizer film formed on the bottom surface of the first substrate, and a second polarizer film formed on the top surface of the second substrate, the first and second polarizer

films being polarized either in the horizontal and vertical directions or in the vertical and horizontal directions.

24. The liquid crystal display of claim 22, further comprising a first polarizer film formed on the bottom surface of the first substrate, and a second polarizer film formed on the top surface of the second substrate, the first and second polarizer films being polarized either in the horizontal and vertical directions or in the vertical and horizontal directions.

25. The liquid crystal display of claim 21, wherein the width of the first to sixth openings is in the range of 10-16 μ m

26. The liquid crystal display of claim 22, wherein the width of the first to sixth openings is in the range of 10-16 μ m

27. A liquid crystal display, comprising:
a first insulating substrate having a top surface and a bottom surface;
a pixel electrode formed on the top surface of the first insulating substrate, the pixel electrode having a first opening pattern at each pixel area, the pixel electrode with the first opening pattern being substantially rectangular in shape in each pixel area and having (a) a first long side and a second long side, (b) a first short side and a second short side, (c) a first corner and a fourth corner formed respectively by ends of the first short side and the first long side and by the second short side and the first long side, (d) a second corner and a third corner respectively formed by the first short side and the second long side and by the second short side and the second long side, (e) an upper region corresponding to roughly half of the pixel electrode defined by the first and second long sides and the first short side, and (f) a lower region corresponding to

roughly half of the pixel electrode defined by the first and second long sides and the second short side;

a second insulating substrate having a top surface and a bottom surface;

5 a common electrode formed on the bottom surface of the second insulating substrate, the common electrode having a second opening pattern at each pixel area, the common electrode with the second opening pattern being substantially rectangular in shape in each pixel area and having (a) a third long side and a fourth long side, (b) a third short side and a fourth short side, (c) an upper region corresponding to roughly half of the common electrode defined by the third long side and the fourth long side and the third short side, and (d) a lower region corresponding to roughly half of the pixel electrode defined by the third long side and the fourth long side and the fourth short side; and

a liquid crystal layer sandwiched between the first substrate and the second substrate while contacting the pixel electrode and the common electrode,

wherein the first opening pattern comprises a first opening, the first opening comprising a first inclined line proceeding from the first long side toward the second long side, and a second inclined line extended from the first inclined line toward the first long side,

20 wherein the second opening pattern comprises second and third openings, the second opening comprising a base proceeding in a direction parallel to the first long side and the second long side, and a projection extended from the base in a direction parallel to the first short side and the second short side, the third opening comprising a body proceeding in a direction parallel to the first short side and the second short side, and a

first limb and a second limb extended from a top end and a bottom end of the body at a predetermined incline while being symmetrical to each other with respect to the second opening, and

wherein the first opening pattern and the second opening pattern are alternately arranged when viewed from above.

28. The liquid crystal display of claim 27, further comprising a first polarizer film formed on the bottom surface of the first substrate, and a second polarizer film formed on the top surface of the second substrate, the first polarizer film and the second polarizer film having polarizing directions either in the horizontal direction and the vertical direction or in the vertical direction and the horizontal direction.

29. The liquid crystal display of claim 27, wherein a distance from the first opening to the third opening is in the range of 10-16 μ m

30. The liquid crystal display of claim 25, (wherein a distance from the first opening to the third opening is in the range of 10-16 μ m

31. The liquid crystal display of claim 27, wherein the second top and bottom edges of the pixel electrode, and edges of the pixel electrode where the base and the projection meet are cut away.

32. The liquid crystal display of claim 28, wherein the second top and bottom edges of the pixel electrode, and edges of the pixel electrode where the base and the projection meet are cut away.

33. A liquid crystal display, comprising:

a first insulating substrate having a top surface and a bottom surface;

a pixel electrode formed on the top surface of the first insulating substrate, the pixel electrode having a first opening pattern at each pixel area, the pixel electrode with the first opening pattern being substantially rectangular in shape and having (a) a first long side and a second long side, (b) a first short side and a second short side, (c) a first corner and a fourth corner formed respectively by ends of the first short side and the first long side and by the second short side and the first long side, (d) a second corner and a third corner formed respectively by the first short side and the second long side and by the second short side and the second long side, (e) an upper region corresponding to roughly half of the pixel electrode defined by the first long side and the second long side and the first short side, and (f) a lower region corresponding to roughly half of the pixel electrode defined by the first long side and the second long side and the second short side;

a second insulating substrate having a top surface and a bottom surface;

a common electrode formed on the bottom surface of the second insulating substrate, the common electrode having a second opening pattern at each pixel area, the common electrode with the second opening pattern being substantially rectangular in shape in each pixel area and having (a) a third long side and a fourth long side, (b) a third short side and a fourth short side, (c) an upper region corresponding to roughly half of the common electrode defined by the third long side and the fourth long side and the third short side, and (d) a lower region corresponding to roughly half of the pixel electrode defined by the third long side and the fourth long side and the fourth short side; and

a liquid crystal layer sandwiched between the first substrate and the second substrate while contacting the pixel electrode and the common electrode,

wherein the first opening pattern comprises a first opening hollowed from the first long side and extending toward the second long side in a direction parallel to the first short side and the second short side, and a second opening hollowed from the second long side and extending toward the first long side parallel to the first opening,

wherein the second opening pattern comprises a third opening, the third opening comprising a first inclined line proceeding in a first direction, a second inclined line extending from the first inclined line in a second direction opposite to the first direction, and a third inclined line extending from the second inclined line parallel to the first inclined line, and

wherein the first and second openings partition the pixel electrode into three separate regions, and the third opening partitions each region of the pixel electrode into two sub-regions.

34. The liquid crystal display of claim 33, further comprising a first polarizer film formed on the bottom surface of the first substrate, and a second polarizer film formed on the top surface of the second substrate, the first and second polarizer films having polarization directions either of 45° and 135° or of 135° and 45° with respect to the short sides of the pixel electrode.

35. The liquid crystal display of claim 33, wherein a distance between the first opening and the third opening is in the range of 10-16 μ m

36. The liquid crystal display of claim 34, wherein a distance between the first opening and the third opening is the range of 10-16 μ m

37. The liquid crystal display of claim 33, wherein edges of the pixel electrode at inlet areas of the first and second openings as well as the second corner and the third corner of the pixel electrode are cut away.

38. The liquid crystal display of claim 34, wherein edges of the pixel electrode at inlet areas of the first and second openings as well as the second corner and the third corner of the pixel electrode are cut away.

39. A liquid crystal display, comprising:

a first insulating substrate having a top surface and a bottom surface;

a pixel electrode formed on the top surface of the first insulating substrate, the pixel electrode having a first opening pattern at each pixel area, the pixel electrode with the first opening pattern being substantially rectangular in shape and having long sides and short sides;

a second insulating substrate having a top surface and a bottom surface;

a common electrode formed on the bottom surface of the second insulating substrate, the common electrode having a second opening pattern at each pixel area, which correspond to each pixel area of the pixel electrode; and

a liquid crystal layer sandwiched between the first substrate and the second substrates while contacting the pixel electrode and the common electrode,

wherein the first opening pattern comprises a plurality of linear openings spaced apart at a predetermined distance and arranged in parallel in a direction parallel to the long sides,

wherein the second opening pattern comprises a plurality of X-shaped openings spaced apart from each other at a predetermined distance and arranged in parallel in a direction parallel to the long sides, and

wherein the first opening pattern partitions the pixel electrode into a plurality of regions, and the second opening pattern partitions each region of the pixel electrode into four sub-regions.

40. The liquid crystal display of claim 39, further comprising a first polarizer film formed on the bottom surface of the first substrate, a second polarizer film formed on the top surface of the second substrate, the first polarizer film and the second polarizer film having polarizing direction either of 45° and 135° or of 135° and 45° with respect to the short sides of the pixel electrode.

41. The liquid crystal display of claim 39, wherein the width of the openings of the first opening pattern and the second opening pattern is in the range of 10 to 16μm

42. The liquid crystal display of claim 40, wherein the width of the openings of the first opening pattern and the second opening pattern is in the range of 10 to 16μm.

43. A liquid crystal display, comprising:
a first insulating substrate having a top surface and a bottom surface;
a pixel electrode formed on the top surface of the first insulating substrate, the pixel electrode having a first opening pattern at each pixel area, the pixel electrode with the first opening pattern being substantially rectangular in shape in each pixel area and having a first long side and a second long side, a first short side and a second short side,

an upper region corresponding to roughly half of the pixel electrode defined by the first long side and the second long side and the first short side, and a lower region corresponding to roughly half of the pixel electrode defined by the first long side and the second long side and the second short side;

5 a second insulating substrate having a top surface and a bottom surface;

a common electrode formed on the bottom surface of the second insulating substrate, the common electrode having a second opening pattern at each pixel area, which correspond to each pixel area of the pixel electrode; and

a liquid crystal layer sandwiched between the first substrate and the second substrate while contacting the pixel electrode and the common electrode,

wherein the first opening pattern comprises a first opening proceeding in a direction parallel to the first long side and the second long side, and second openings positioned under the first opening portion while proceeding in a direction parallel to the first short side and the second short side, the second opening partitioning the pixel electrode into the upper region and the lower region,

wherein the second opening pattern comprises third openings proceeding in a direction parallel to the first long side and the second long side, and fourth openings positioned below the third opening while proceeding in a direction parallel to the first short side and the second short side,

20 wherein the first opening and the third opening are alternately arranged while partitioning the upper region of the pixel electrode into a plurality of sub-regions in a direction parallel to the first long side and the second long side, and the second opening and the fourth opening are alternately arranged while partitioning the lower region of the

pixel electrode into a plurality of sub-regions in a direction parallel to the first short side and the second short side.

44. The liquid crystal display of claim 43, wherein the fourth opening closest to the second short side overlap the second short side of the pixel electrode.

45. The liquid crystal display of claim 43, wherein both ends of the fourth openings have a gradually enlarged width.

46. The liquid crystal display of claim 43, wherein a bottom end of the first opening has a gradually reduced width.

47. The liquid crystal display of claim 43, further comprising a first polarizer film formed on the bottom surface of the first substrate, and a second polarizer film formed on the top surface of the second substrate, the first polarizer film and the second polarizer film having polarizing directions either of 45° and 135° or of 135° and 45° with respect to the short sides of the pixel electrode.

48. The liquid crystal display of claim 43, wherein the width of the first through the fourth openings is in the range of 10 to 16 μ m

49. A liquid crystal display, comprising:

a first insulating substrate having a top surface and a bottom surface;

a pixel electrode formed on the top surface of the first insulating substrate, the pixel electrode having a first opening pattern at each pixel area, the pixel electrode with the first opening pattern being substantially rectangular in shape in each pixel area and having (a) a first long side and a second long side, (b) a first short side and a second short side, (c) an upper region corresponding to roughly half of the pixel electrode defined by the first long side and the second long side and the first short side, and (d) a

lower region corresponding to roughly half of the pixel electrode defined by the first long side and the second long side and the second short side;

a second insulating substrate having a top surface and a bottom surface;

5 a common electrode formed on the bottom surface of the second insulating substrate, the common electrode having a second opening pattern at each pixel area, the common electrode with the second opening pattern being substantially rectangular in shape in each pixel area and having (a) a third long side and a second long side, (b) a first short side and a second short side, (c) an upper region corresponding to roughly half of the common electrode defined by the third long side and the fourth long side and the third short side, and (d) a lower region corresponding to roughly half of the pixel electrode defined by the third long side and the fourth long side and the fourth short side; and

a liquid crystal layer sandwiched between the first substrate and the second substrate while contacting the pixel electrode and the common electrode,

wherein the first opening pattern comprises a first linear opening proceeding in a direction parallel to the first short side and the second short side,

wherein the second opening pattern comprises a second opening proceeding in a direction parallel to the first long side and the second long side, and a third opening positioned away from the second opening toward the second short side and proceeding
20 in a direction parallel to the first short side and the second short side, and

wherein the second opening bisects the upper region of the pixel electrode into left and right sub-regions, and the first and third openings partition the lower region of the pixel electrode into a plurality of sub-regions.

50. The liquid crystal display of claim 49, wherein a top end of the second opening portion, and both ends of the third opening portion have a gradually enlarged width.

51. The liquid crystal display of claim 49, wherein the third opening overlaps the second short side of the pixel electrode.

52. The liquid crystal display of claim 49, further comprising a first polarizer film formed on the bottom surface of the first substrate, and a second polarizer film formed on the top surface of the second substrate, the first polarizer film and the second polarizer film having polarizing directions either of 45° and 135° or of 135° and 45° with respect to the short sides of the pixel electrode.

53. The liquid crystal display of claim 49, wherein the width of the first through the third openings is in the range of 10 to 16μm.

54. A liquid crystal display, comprising:
a first insulating substrate having a top surface and a bottom surface;
a pixel electrode formed on the top surface of the first insulating substrate, the pixel electrode at each pixel area being shaped with a plurality of ovals continuously interconnected in a vertical direction;

a second insulating substrate having a top surface and a bottom surface;

a common electrode formed on the bottom surface of the second insulating substrate, the common electrode having an opening pattern at each pixel area, which corresponds to each pixel area of the pixel electrode; and

a liquid crystal layer sandwiched between the first substrate and the second substrate while contacting the pixel electrode and the common electrode,

wherein the opening pattern comprises first diamond-shaped openings spaced apart from each other at a predetermined distance in a longitudinal direction, and second openings and third openings externally surrounding the first diamond-shaped openings,

wherein the region of the common electrode defined by inner sides of the second opening and the third opening facing the diamond-shaped opening portion being formed with oval-shaped portions, each oval-shaped portion surrounding one of the diamond-shaped openings, and

wherein each of the first diamond-shaped openings is positioned at each oval-shaped portion of the pixel electrode when viewed from above, and the second openings and the third openings surround the pixel electrode.

55. The liquid crystal display of claim 54, further comprising a first polarizer film formed on the bottom surface of the first substrate, and a second polarizer film formed on the top surface of the second substrate, the first polarizer film and the second polarizer film having polarizing directions either in the longitudinal direction and perpendicular to the longitudinal direction.

56. The liquid crystal display of claim 54, wherein a distance from the second opening and the third opening to an outer side of the pixel electrode is uniformly in the range of 10-16 μ m.

57. The liquid crystal display of claim 55, further comprising a first compensation film attached between one of the substrates and one of the polarizer films attached thereto.

58. The liquid crystal display of claim 57, wherein the first compensation film is a biaxial compensation film.

59. The liquid crystal display of claim 58, wherein a slow axis of the first compensation film is parallel or perpendicular to the polarizing directions of the polarizer films.

60. The liquid crystal display of claim 57, further comprising a second compensation film attached between one of the substrates and one of the polarizer films attached thereto.

61. The liquid crystal display of claim 60, wherein the first compensation film and the second compensation film are respectively an a-plate compensation film and a c-plate compensation film.

62. The liquid crystal display of claim 61, wherein a slow axis of the a-plate compensation film is parallel with the polarizing directions of the polarizer film.

63. The liquid crystal display of claim 61, wherein a slow axis of the a-plate compensation film is perpendicular to the polarizing directions of the polarizer film.

64. A substrate for a liquid crystal display, comprising:

a pixel electrode having a saw shape aperture; and

a wiring overlapping the aperture.

65. The substrate of claim 64, wherein the wiring is a gate wiring.

66. A substrate for a liquid crystal display, comprising:

a common electrode having a saw shape aperture; and

a black matrix overlapping the aperture.

67. A liquid crystal display, comprising:

a first substrate including a pixel electrode having a first saw shape aperture;

and

a second substrate opposite the first substrate, the second substrate including a black matrix and a common electrode having a second saw shape aperture that is parallel and alternate to the first aperture,

wherein the black matrix includes a first portion overlapping the second aperture, a second portion put across bent points of the first and the second saw shape aperture and a third portion covering a portion where the first aperture and the second aperture meet a boundary of the pixel electrode.

68. The liquid crystal display of claim 67, further comprising a wiring overlapping the first aperture on the first substrate.

69. The liquid crystal display of claim 68, wherein the wiring is a gate wiring.

70. The liquid crystal display of claim 67, wherein the black matrix further includes a fourth portion overlapping the first aperture.

71. The liquid crystal display of claim 67, wherein the third portion of the black matrix is a triangular shape.

ABSTRACT OF THE DISCLOSURE

A liquid crystal display includes a first insulating substrate with a top surface and a bottom surface. A pixel electrode is formed on the top surface of the first insulating substrate. The pixel electrode has a first opening pattern at each pixel area.

5 The pixel electrode with the first opening pattern is substantially rectangular in shape with a first long side and a second long side, and a first short side and a second short side. The pixel electrode is divided into an upper region defined by the first long side and the second long side and the first short side, and a lower region defined by the first long side and the second long side and the second short side. A second insulating substrate with a top surface and a bottom surface is arranged parallel to the first insulating substrate at a predetermined distance from the same such that the bottom surface of the second insulating substrate faces the top surface of the first insulating substrate. A common electrode is formed on the bottom surface of the second insulating substrate. The common electrode has a second opening pattern at each pixel area, which correspond to each pixel area of the pixel electrode. A liquid crystal layer is sandwiched between the first substrate and the second substrate while contacting the pixel electrode and the common electrode. The first opening pattern and the second opening pattern each have a plurality of openings, the openings of the first opening pattern and the second opening pattern being alternately arranged parallel to each other.

FIG. 1

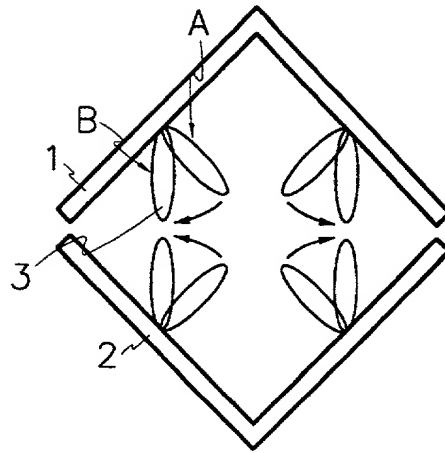


FIG. 2

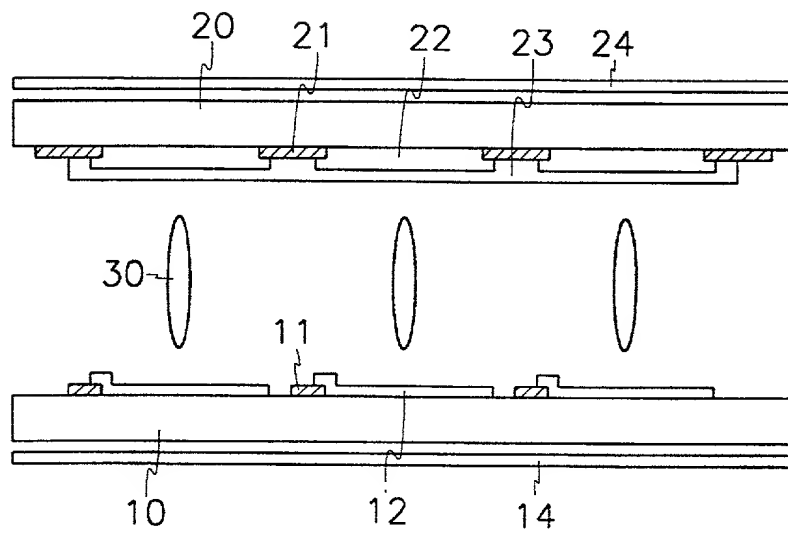


FIG. 3A

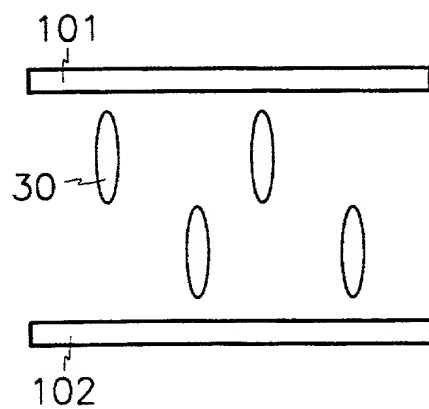


FIG. 3B

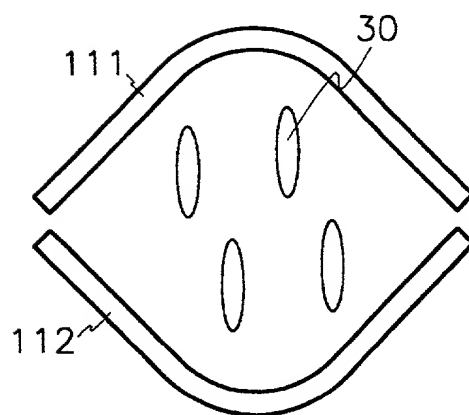


FIG. 4A

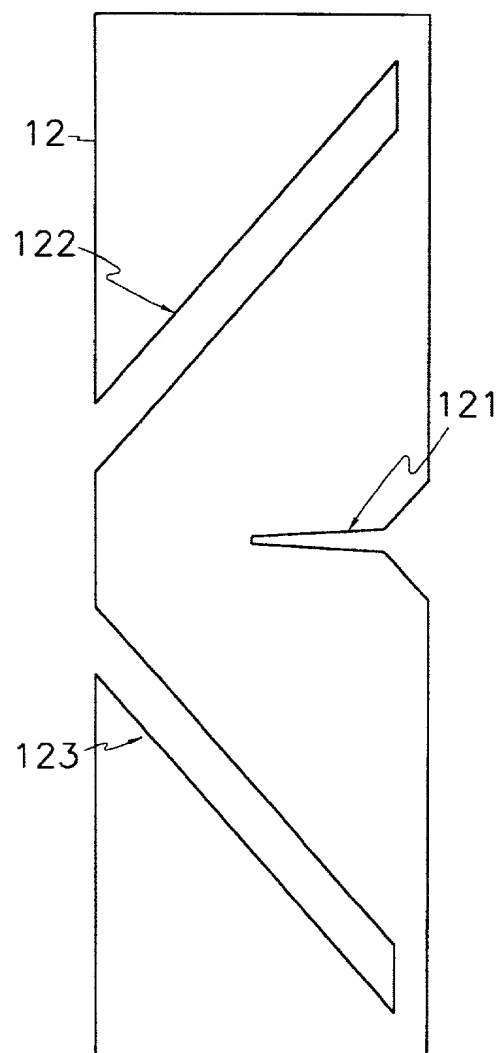


FIG. 4B

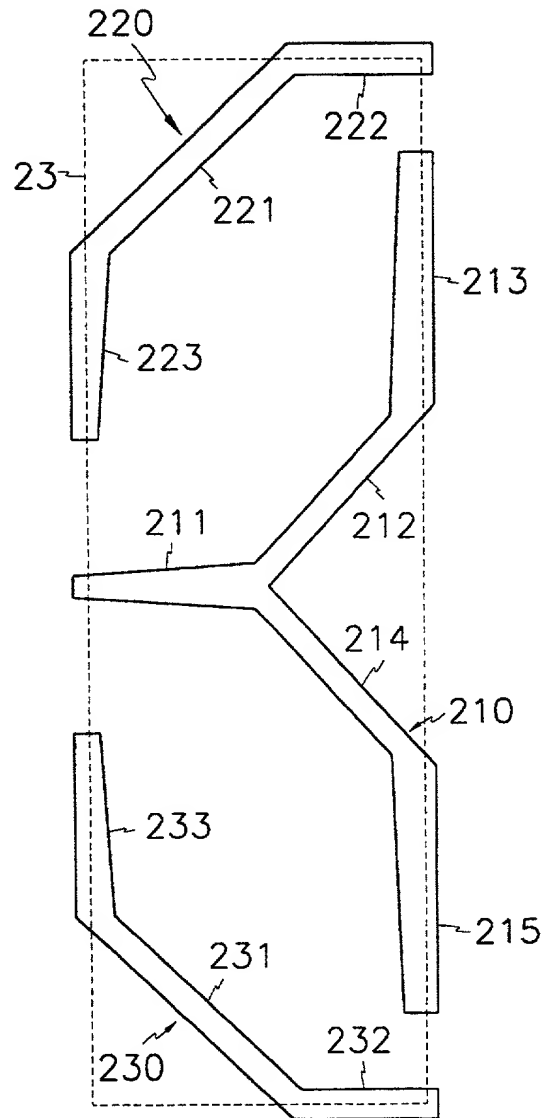


FIG. 4C

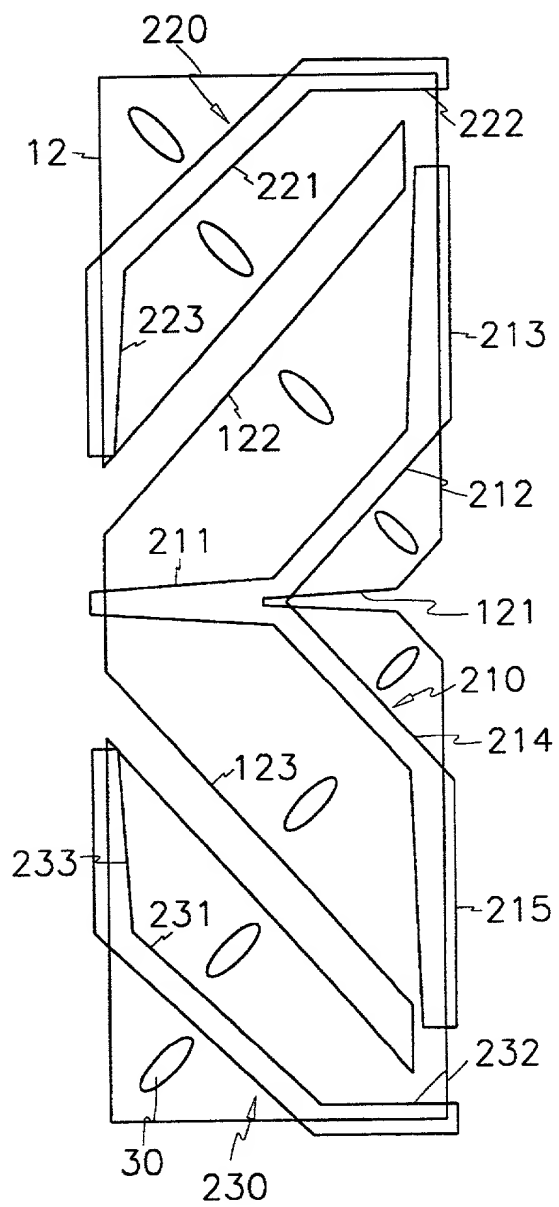


FIG. 5A

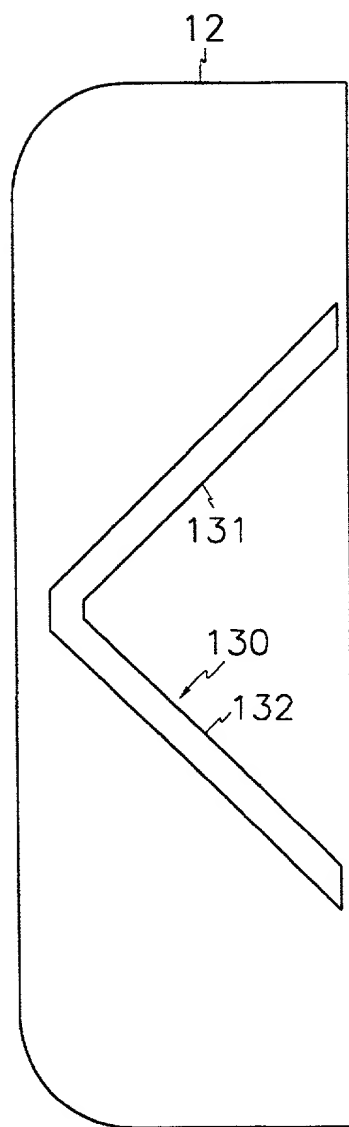


FIG. 5B

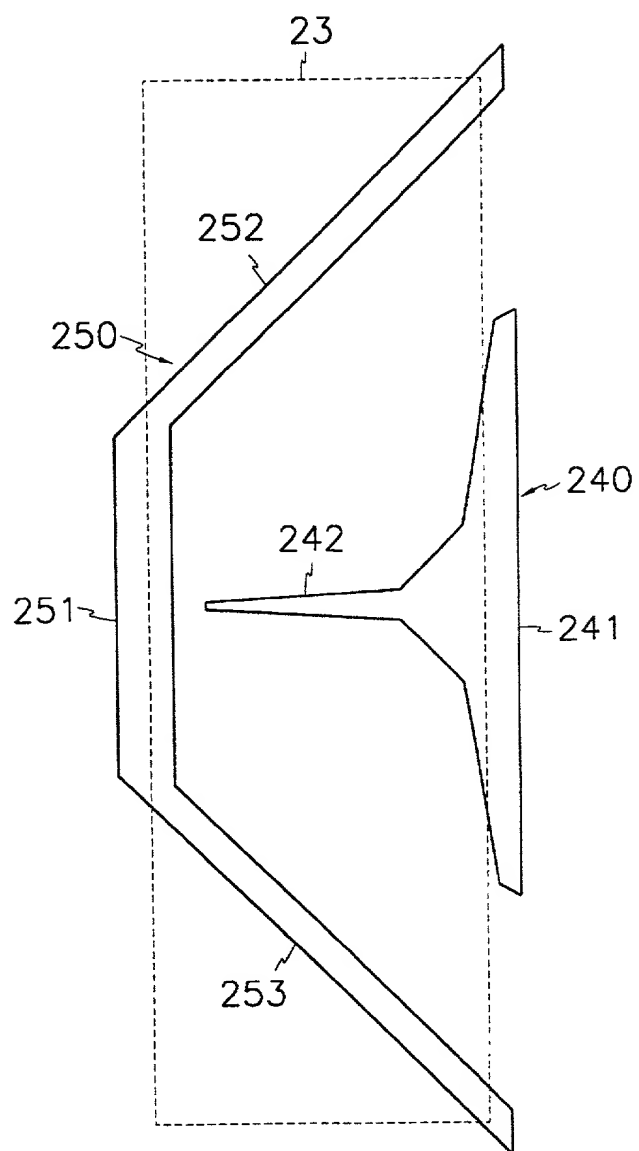


FIG. 5C

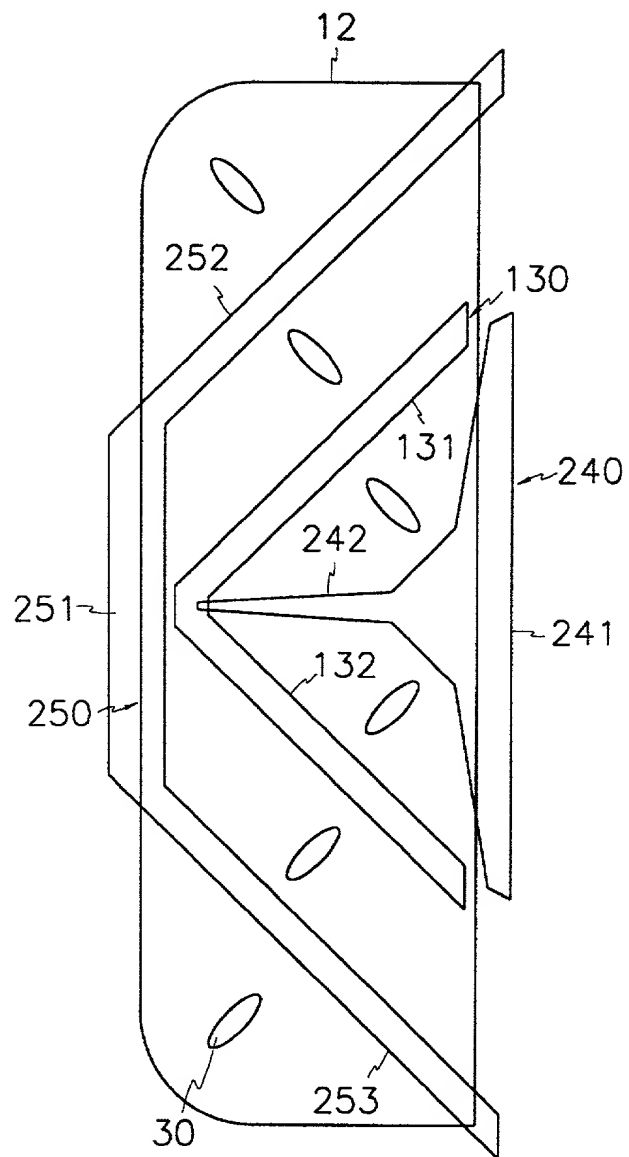


FIG. 6A

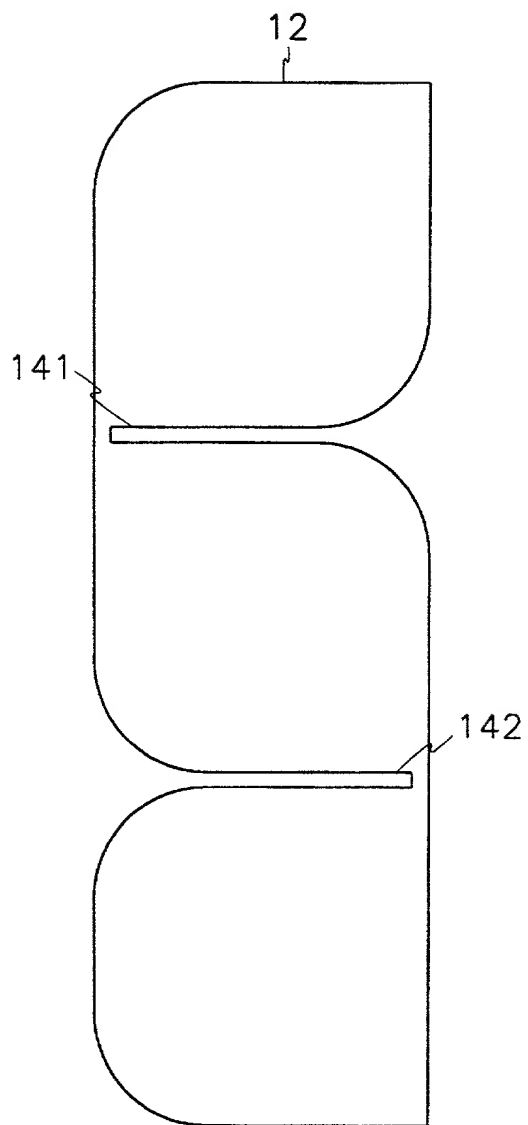


FIG. 6B

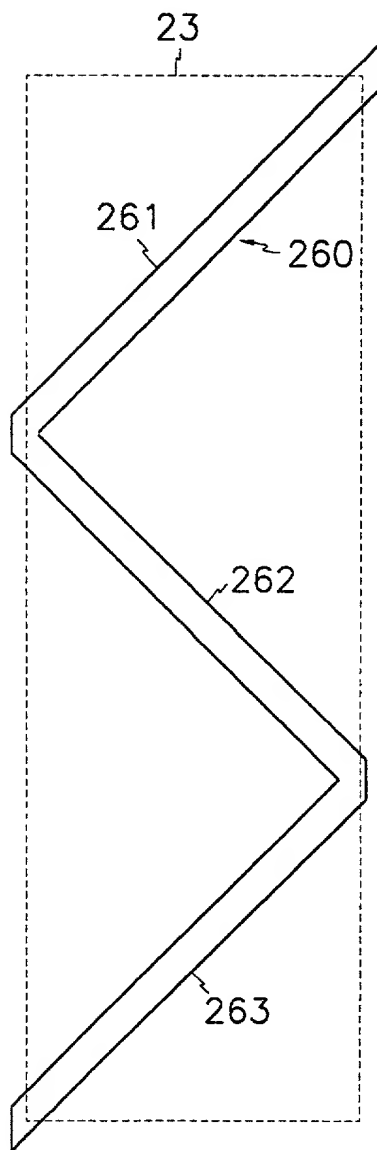


FIG. 6C

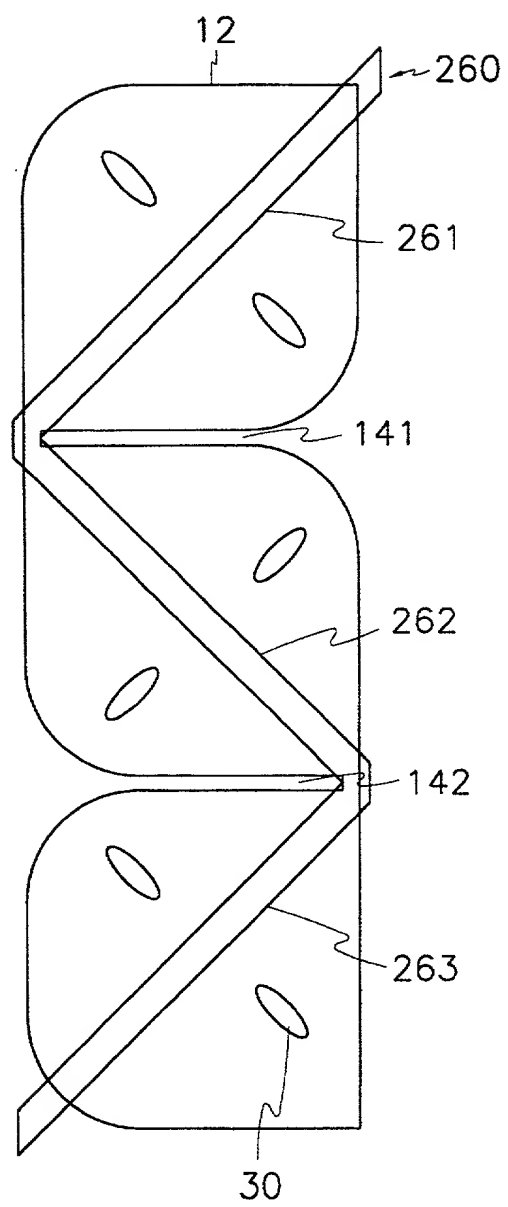


FIG. 7A

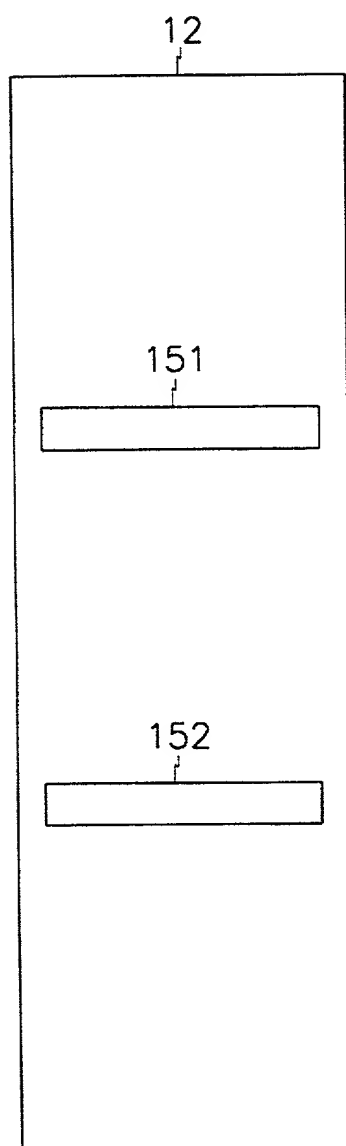


FIG. 7B

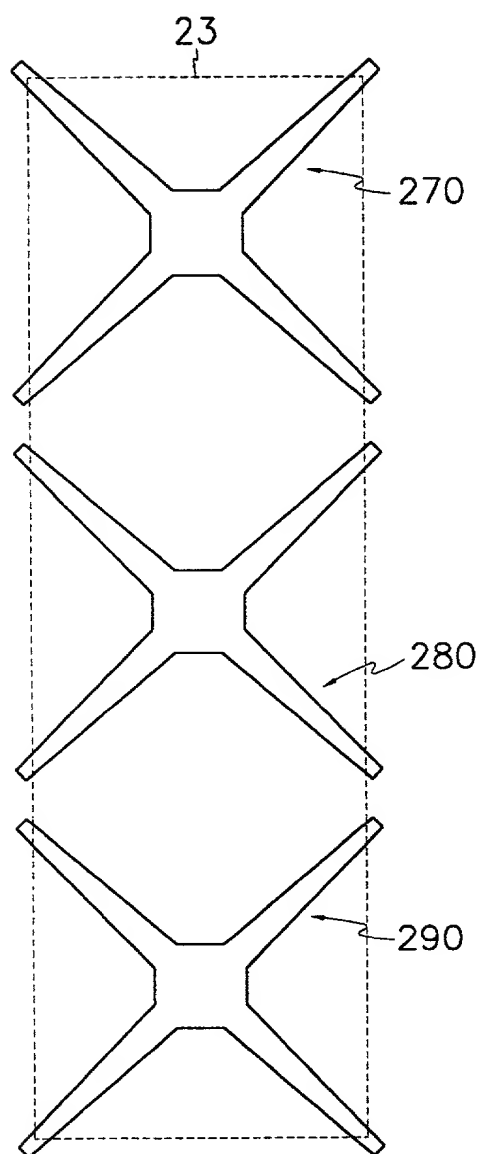


FIG. 7C

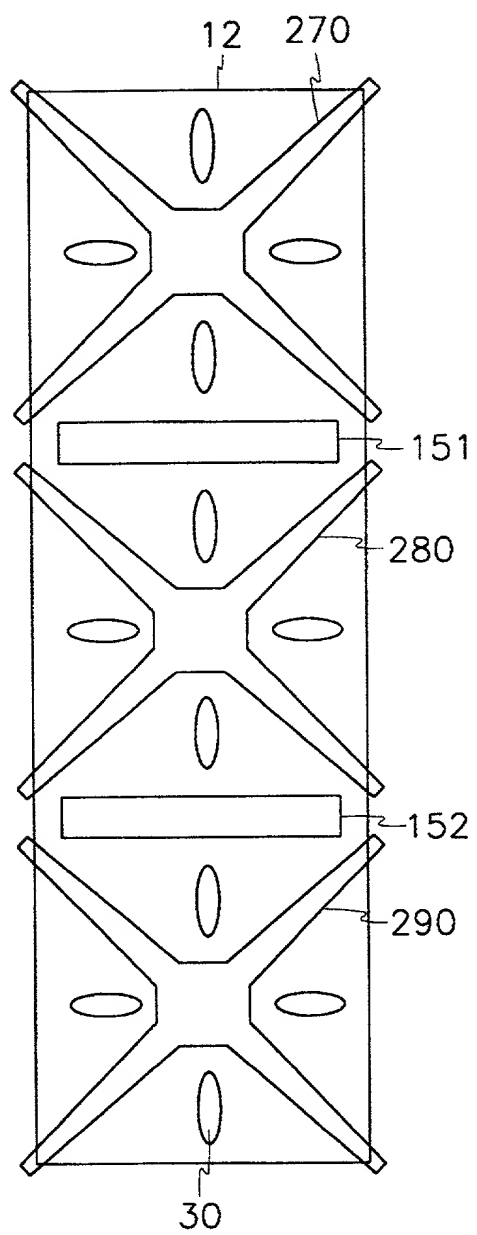


FIG. 8A

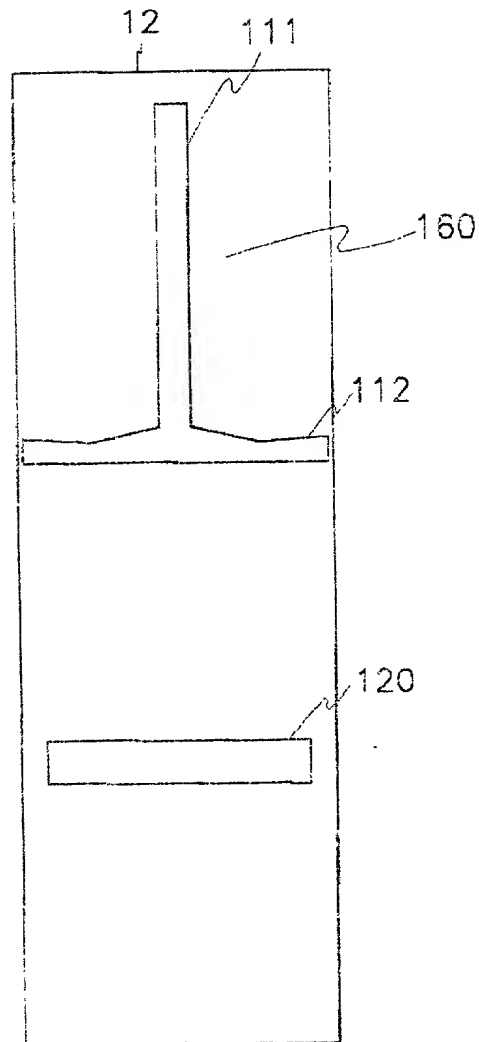


FIG. 8B

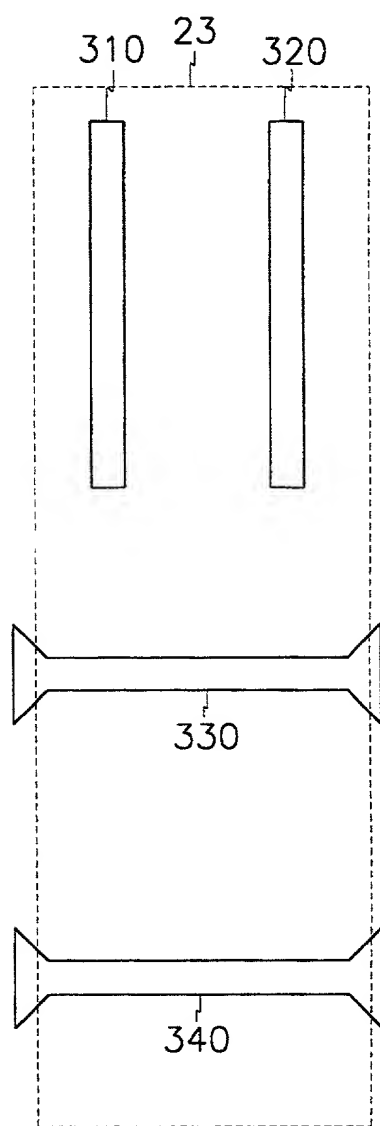


FIG. 8C

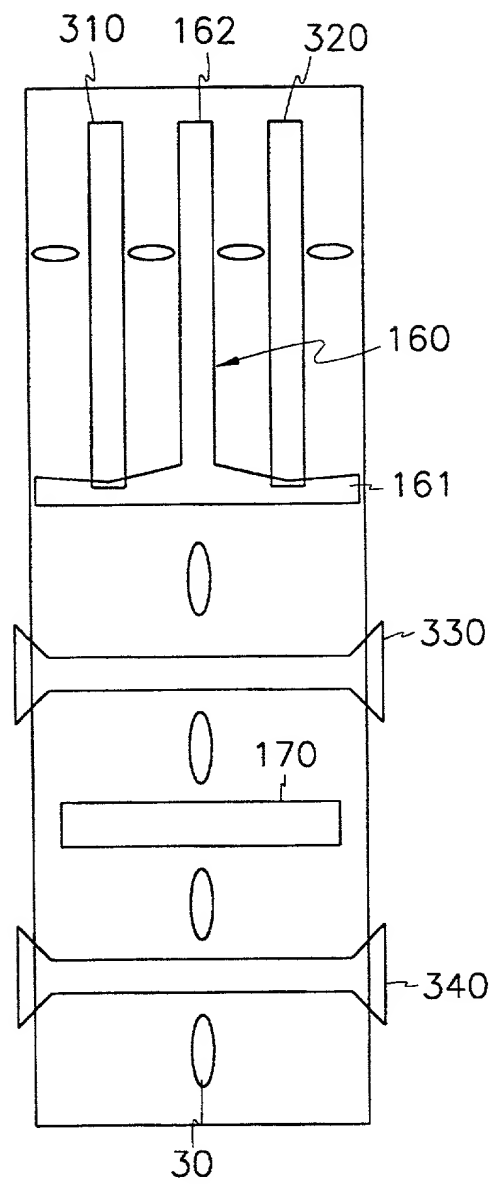


FIG. 9A

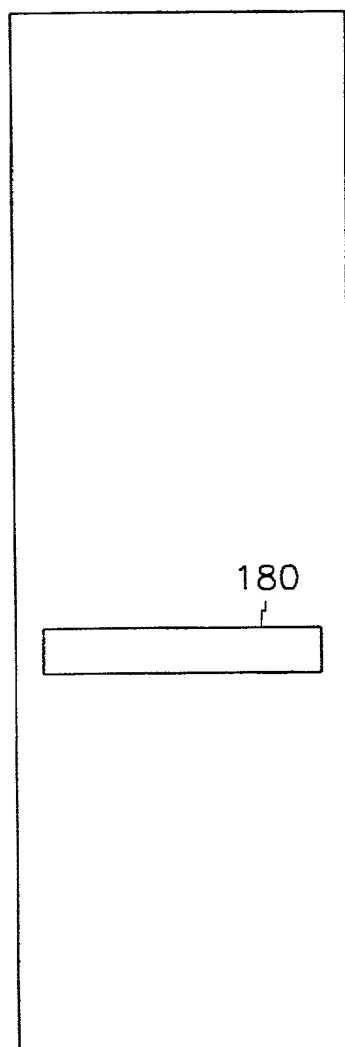


FIG. 9B

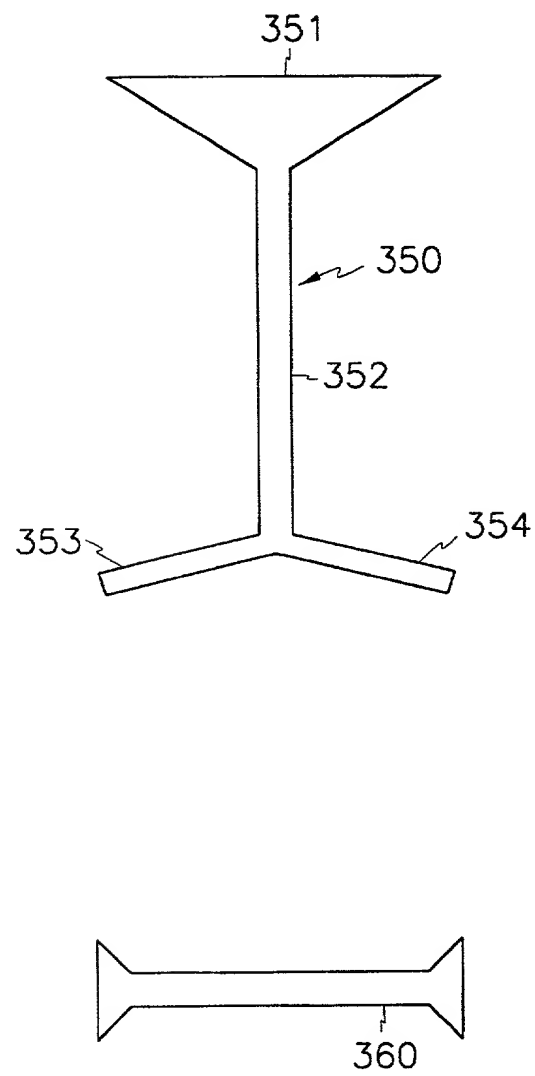


FIG. 9C

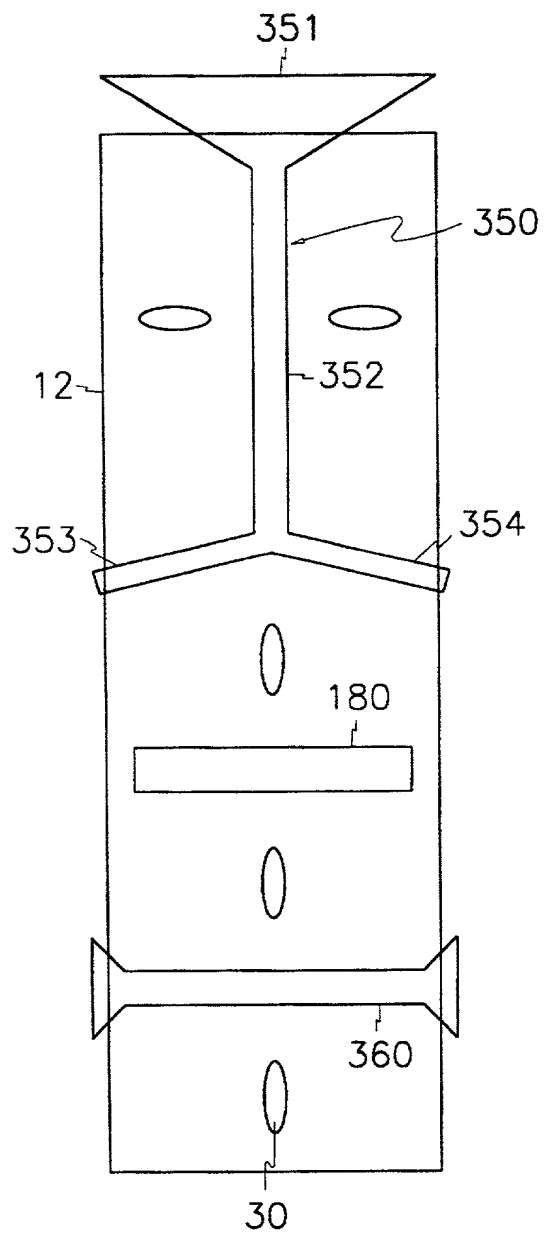


FIG. 10A

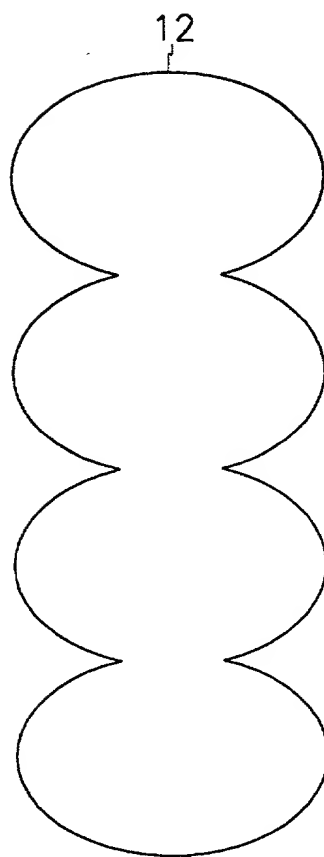


FIG. 10B

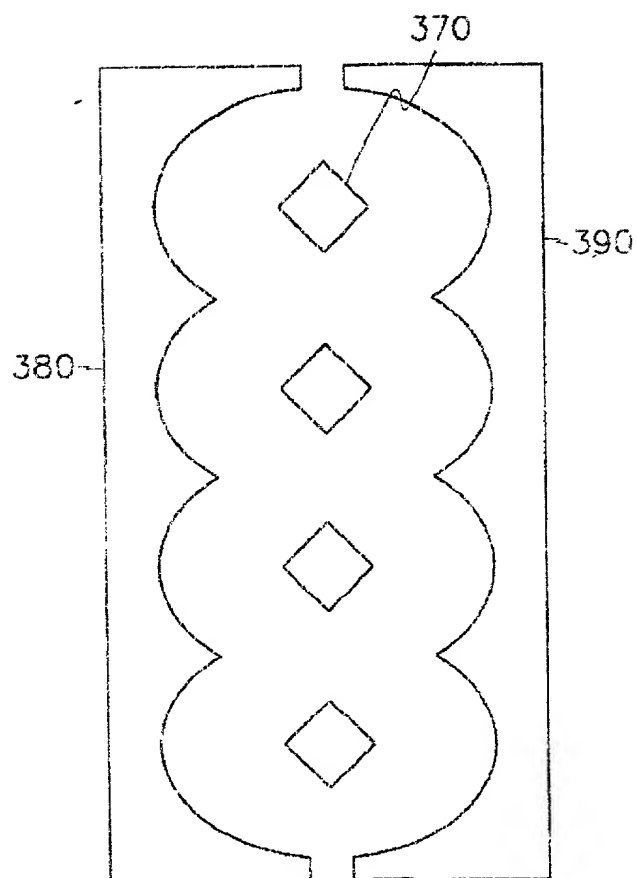


FIG. 10C

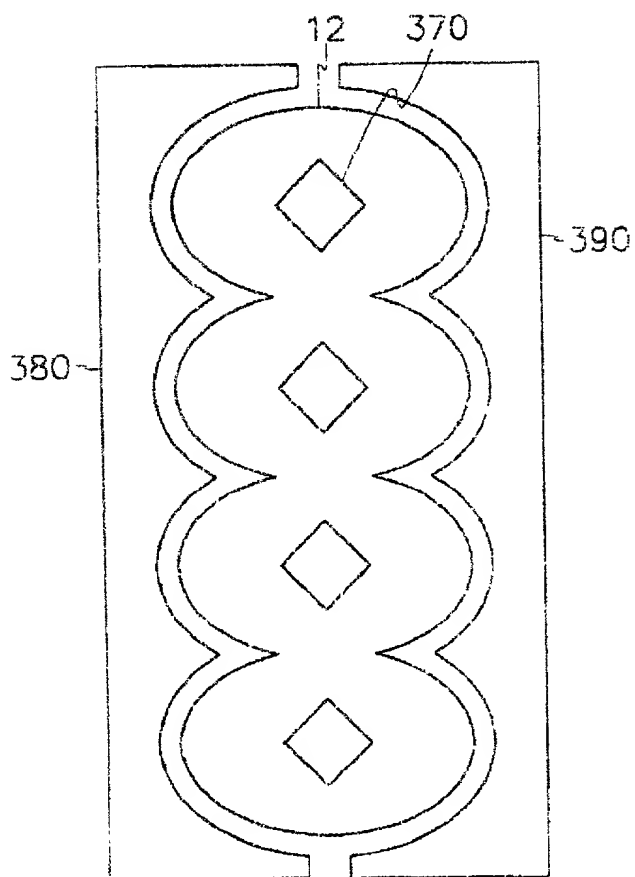


FIG. 11

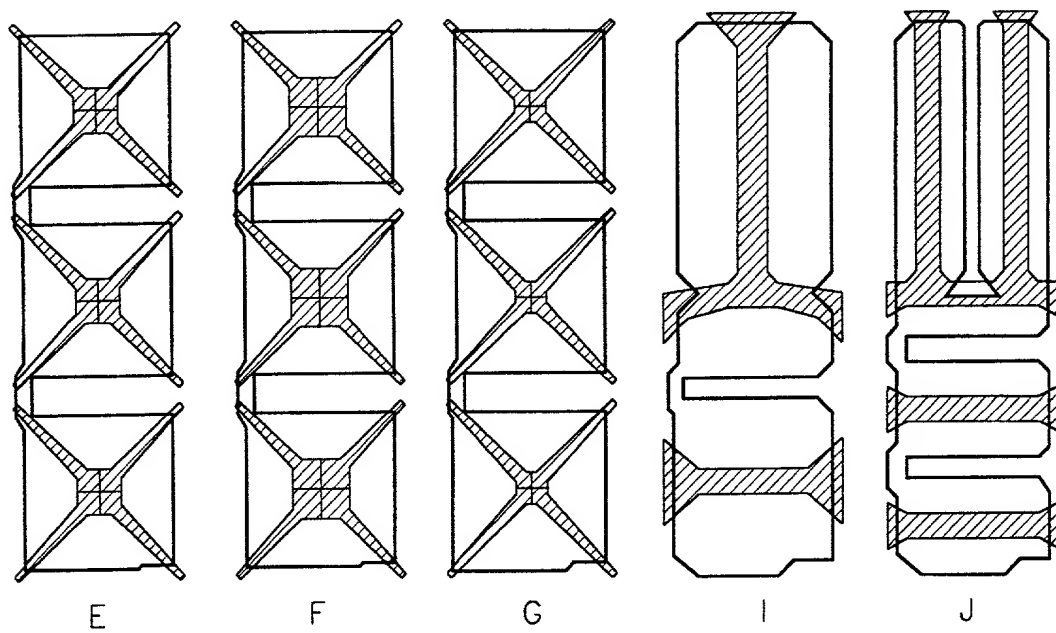
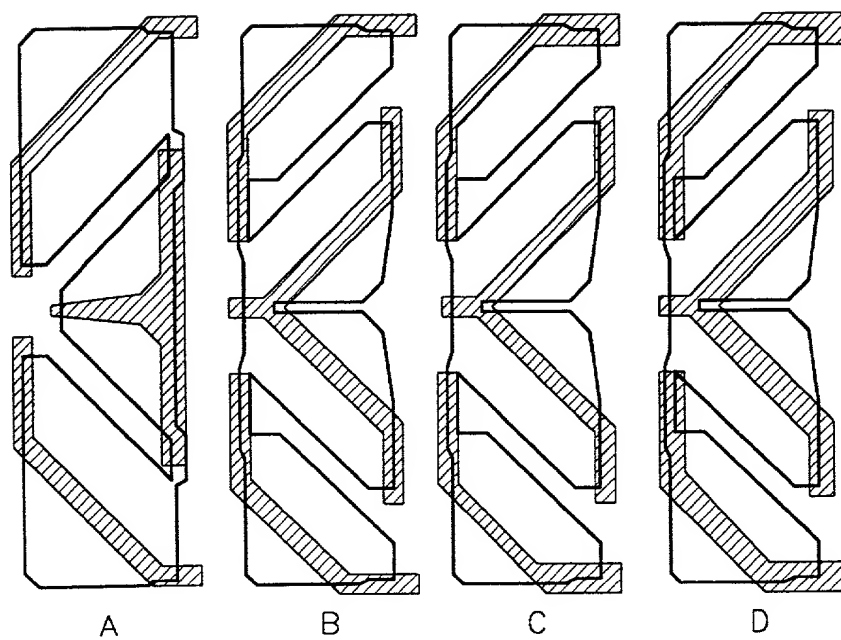
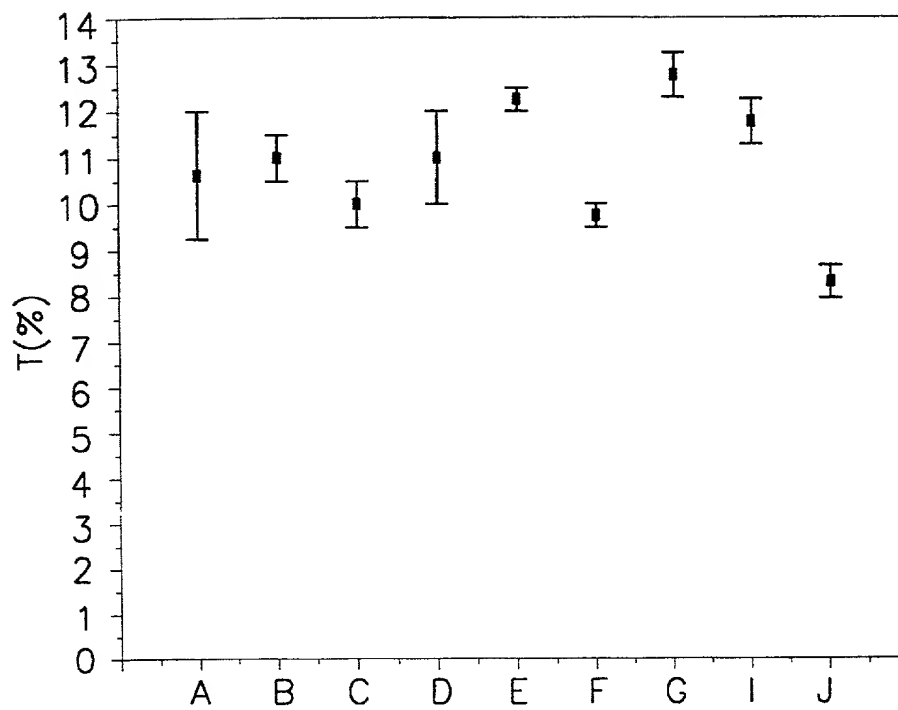


FIG. 12A



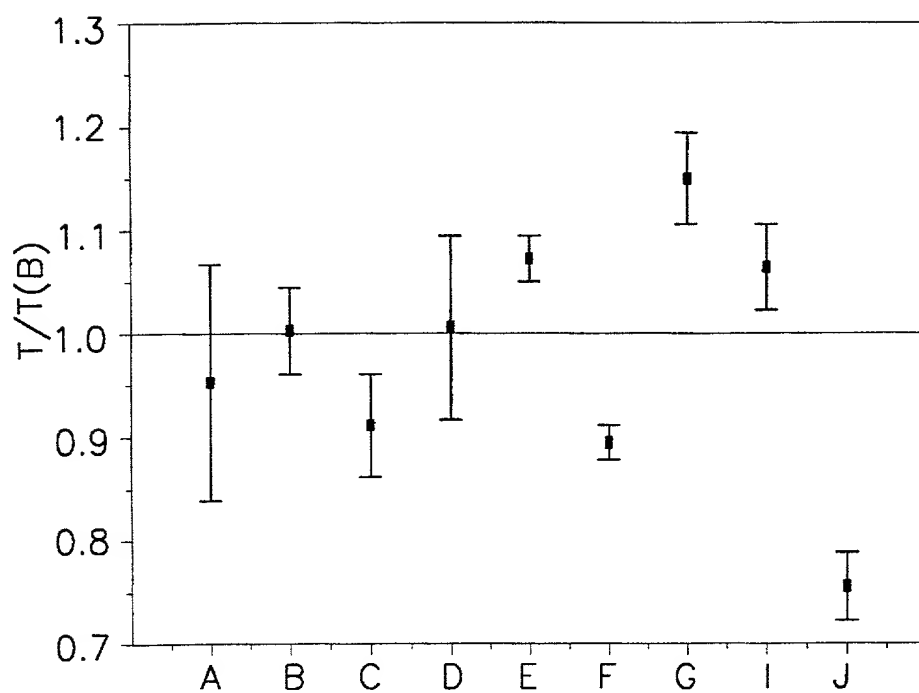
[illegible]

FIG. 13

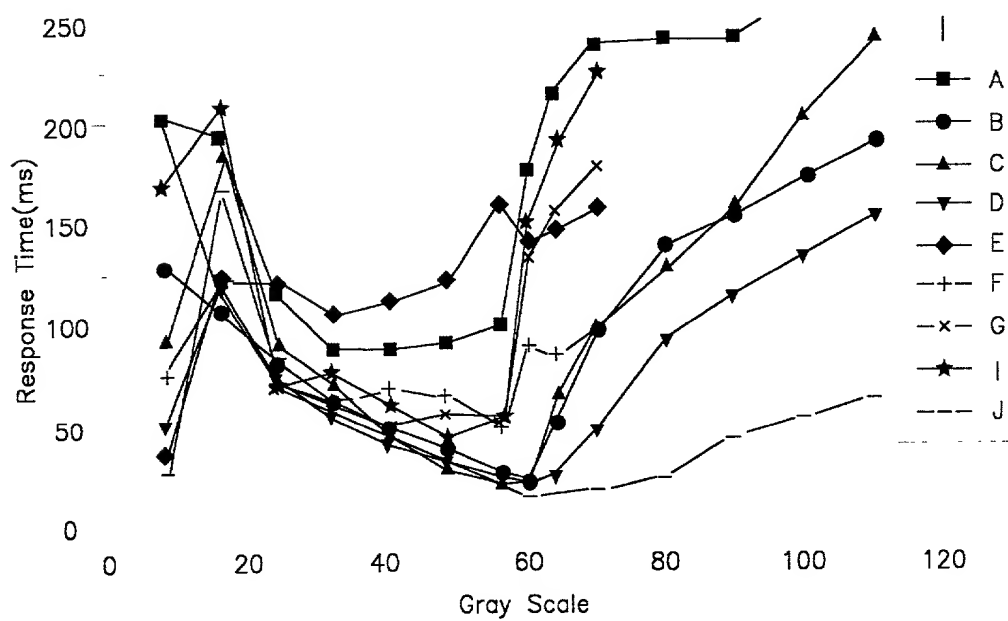


FIG. 14

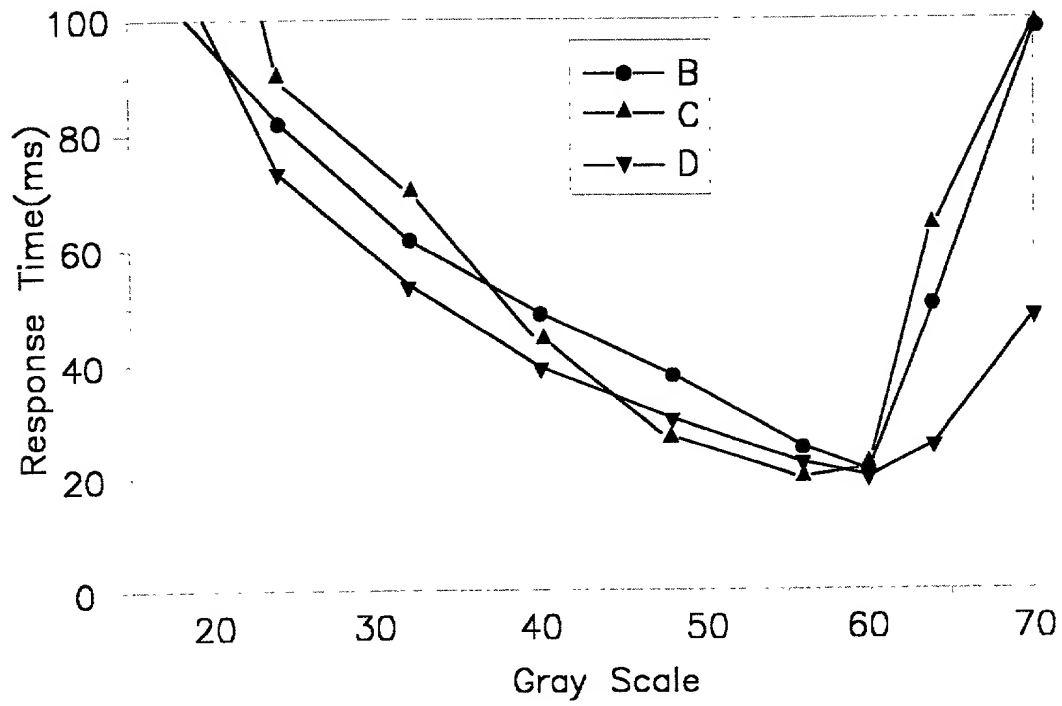


FIG. 15

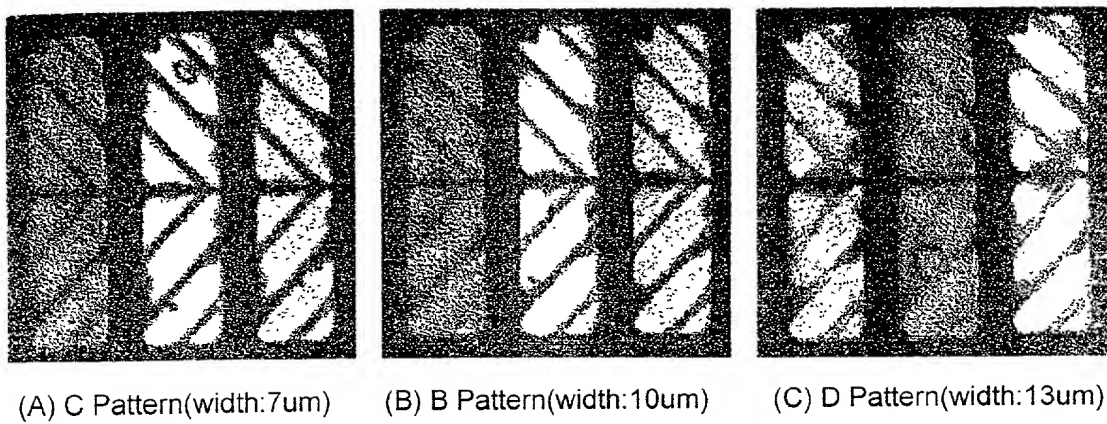
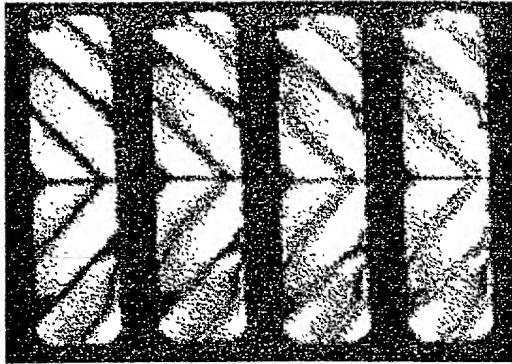
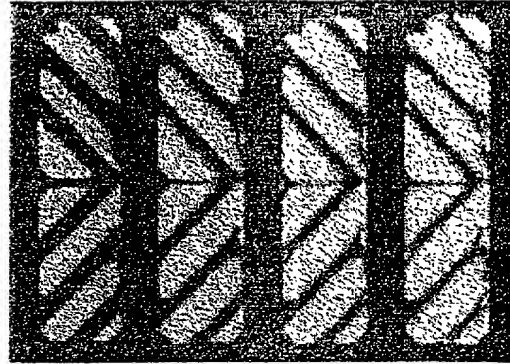


FIG.16



(A) C Pattern:3V→ 3.5V → 4V → 5V



(B) D Pattern:3V→ 3.5V→ 4V→ 5V

FIG. 17A

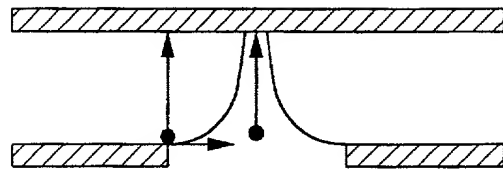


FIG. 17B

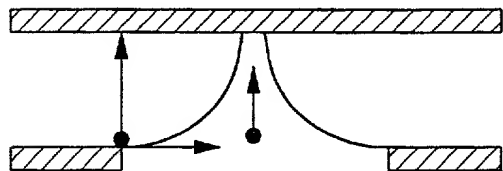
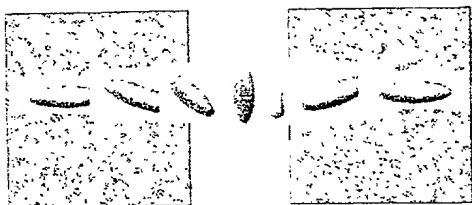
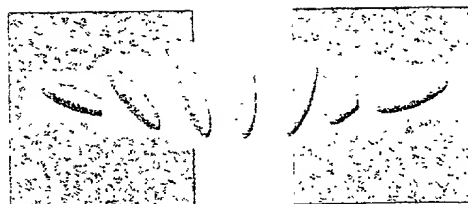


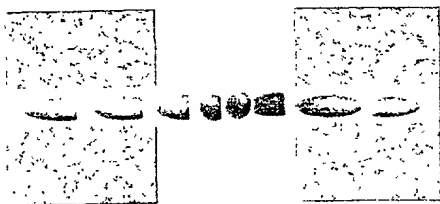
FIG.18



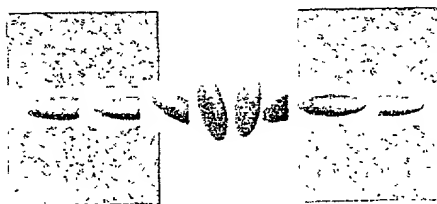
(A) Small Width, Low Voltage



(A) Small Width, High Voltage



(C) Large Width, Low Voltage



(D) Large Width, High Voltage

FIG. 19

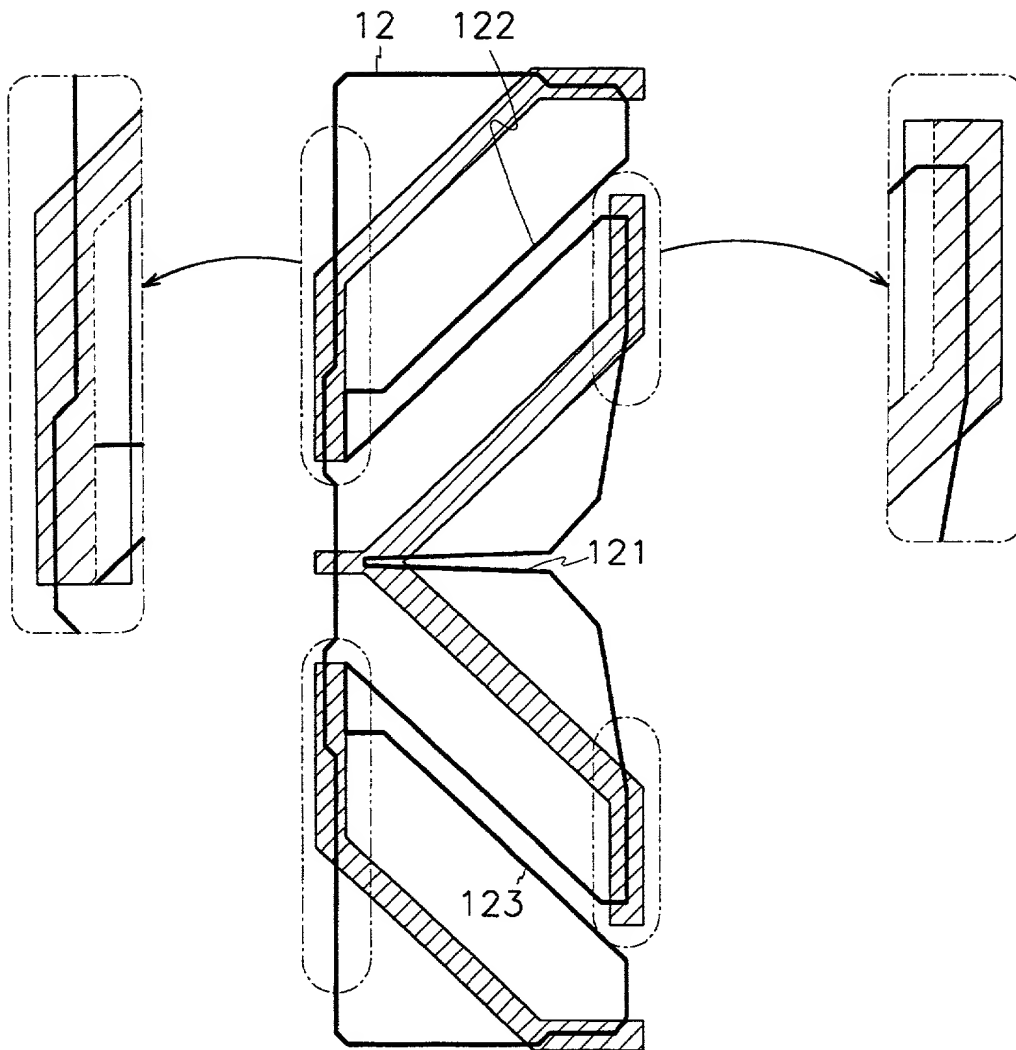


FIG. 20

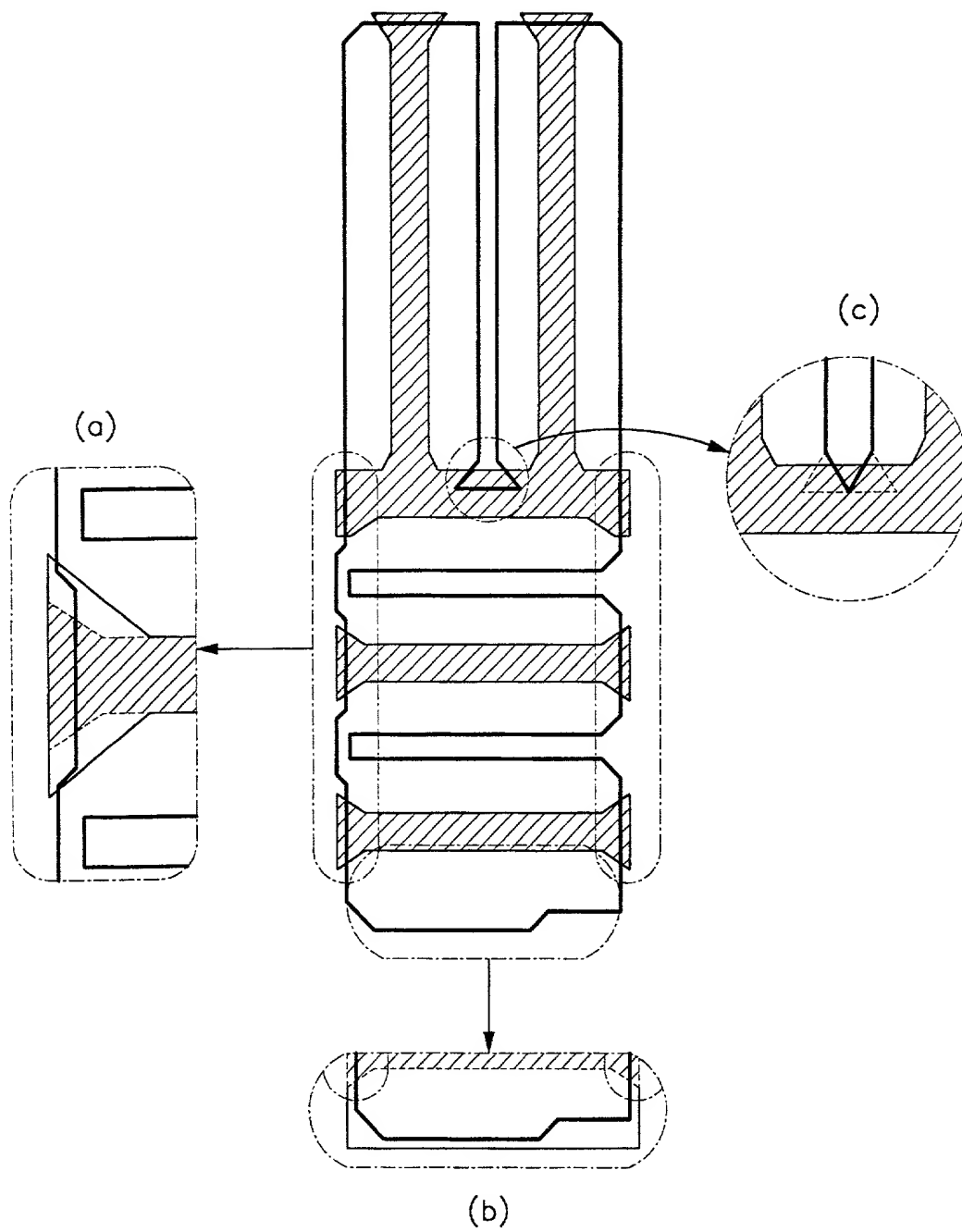


FIG. 21A

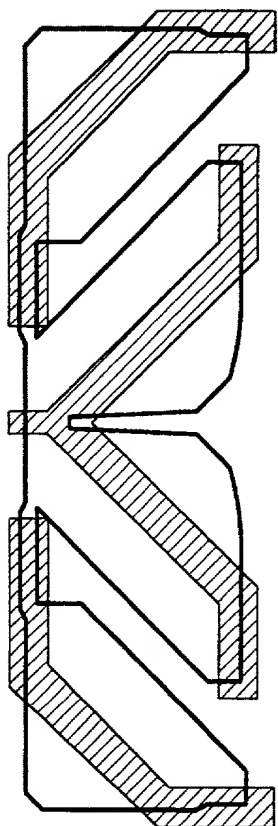


FIG. 21B

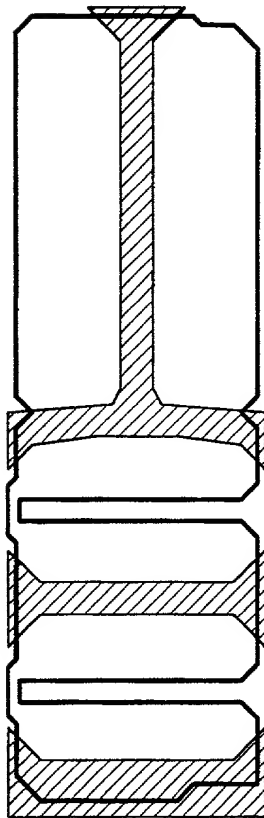
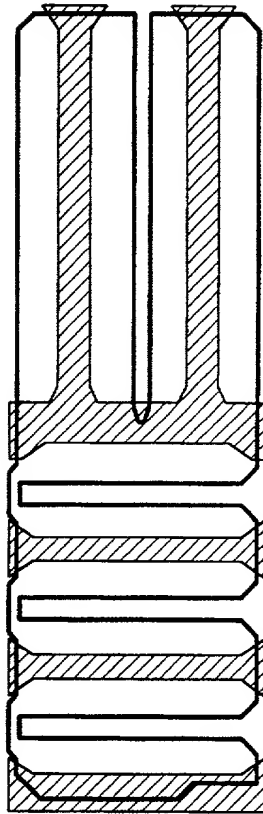


FIG. 21C



Year	1990	1991	1992	1993	1994	1995	1996	1997	1998	1999	2000	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010	2011	2012	2013	2014	2015	2016	2017	2018	2019	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033	2034	2035	2036	2037	2038	2039	2040	2041	2042	2043	2044	2045	2046	2047	2048	2049	2050	2051	2052	2053	2054	2055	2056	2057	2058	2059	2060	2061	2062	2063	2064	2065	2066	2067	2068	2069	2070	2071	2072	2073	2074	2075	2076	2077	2078	2079	2080	2081	2082	2083	2084	2085	2086	2087	2088	2089	2090	2091	2092	2093	2094	2095	2096	2097	2098	2099	2100
1990	1990	1991	1992	1993	1994	1995	1996	1997	1998	1999	2000	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010	2011	2012	2013	2014	2015	2016	2017	2018	2019	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033	2034	2035	2036	2037	2038	2039	2040	2041	2042	2043	2044	2045	2046	2047	2048	2049	2050	2051	2052	2053	2054	2055	2056	2057	2058	2059	2060	2061	2062	2063	2064	2065	2066	2067	2068	2069	2070	2071	2072	2073	2074	2075	2076	2077	2078	2079	2080	2081	2082	2083	2084	2085	2086	2087	2088	2089	2090	2091	2092	2093	2094	2095	2096	2097	2098	2099	2100

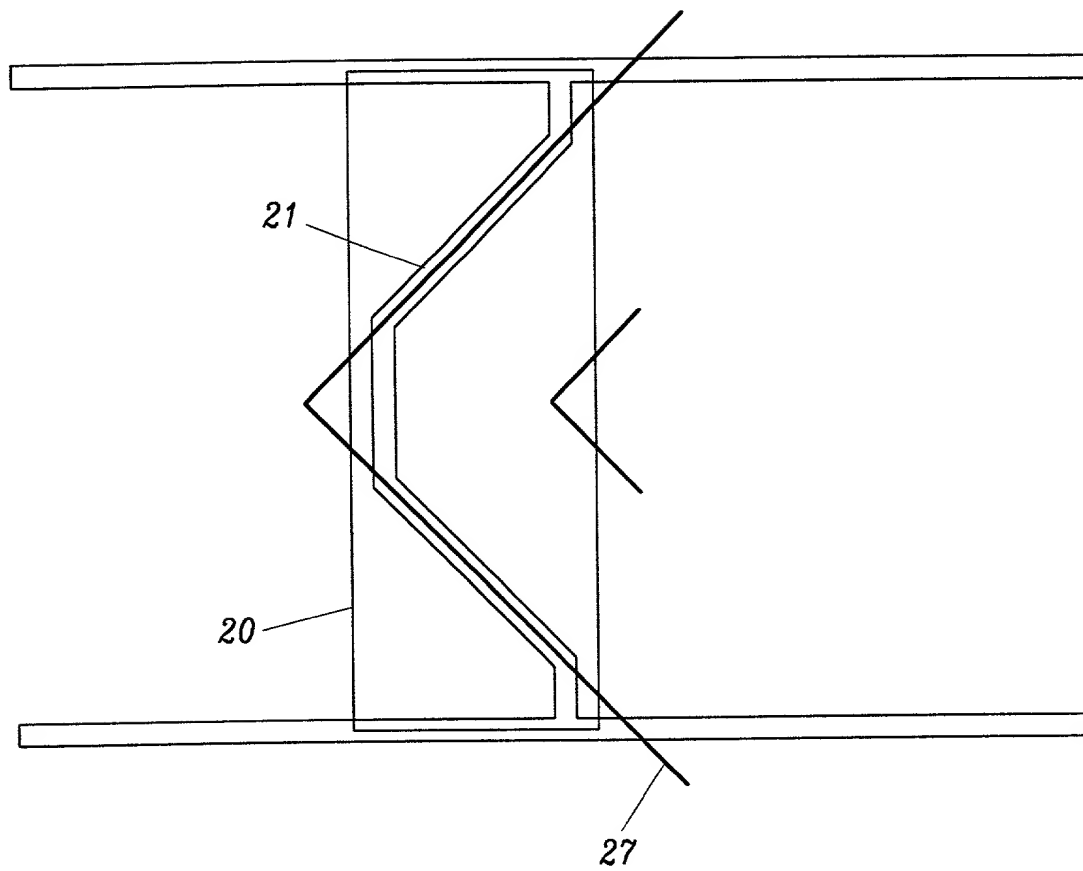


FIG.23

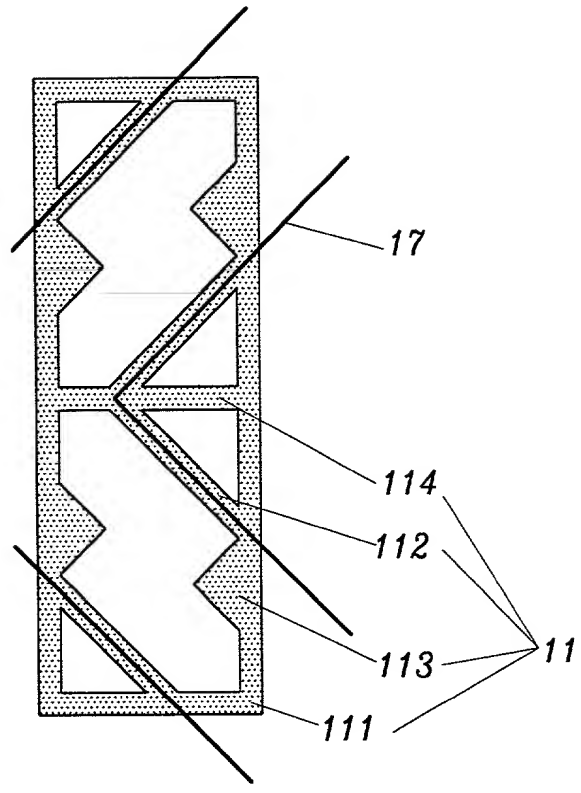


FIG.24

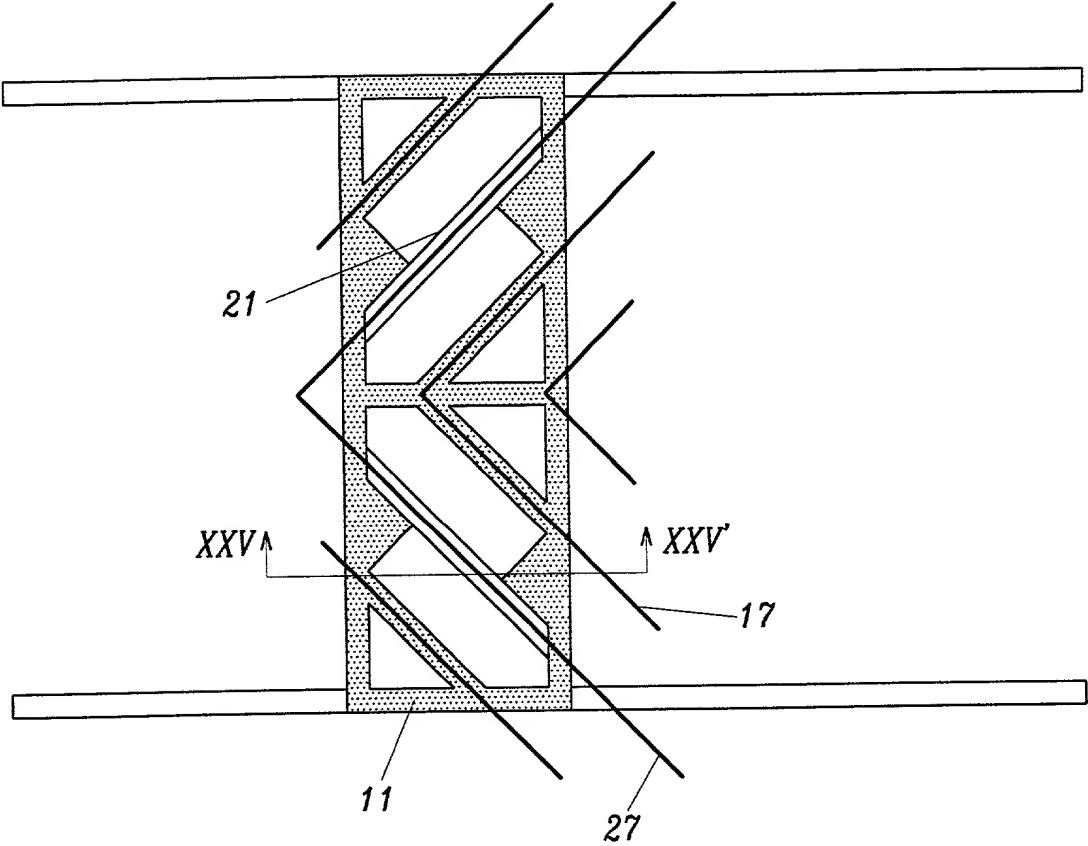


FIG.25

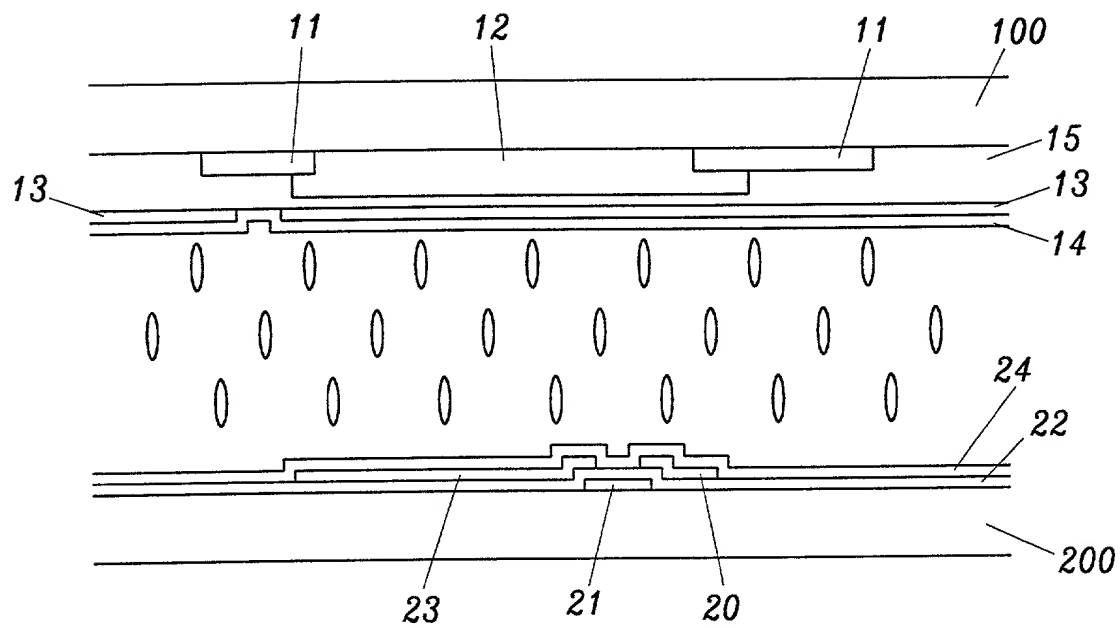


FIG.26

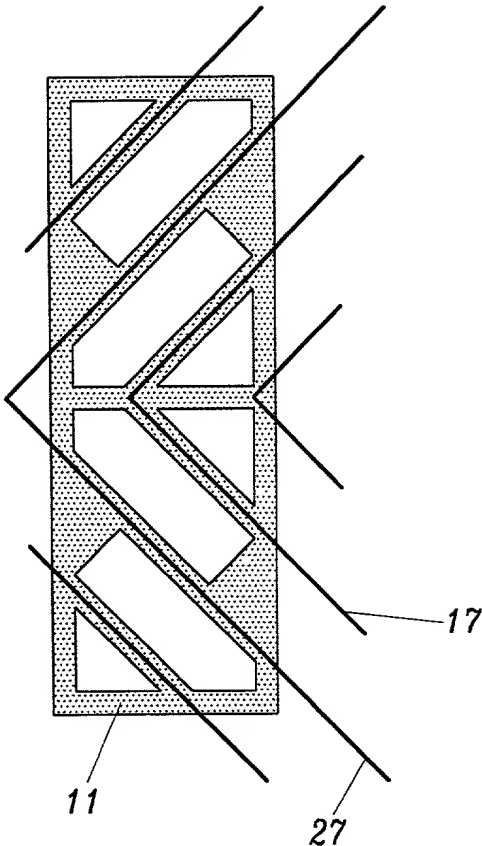


FIG.27

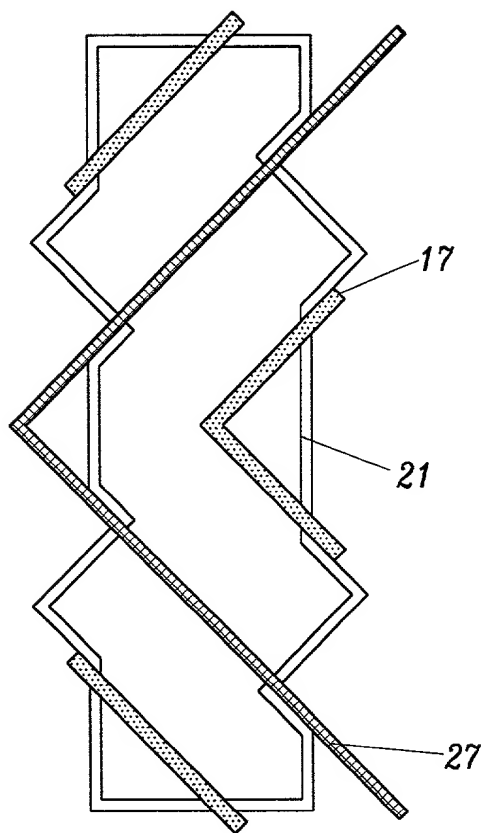
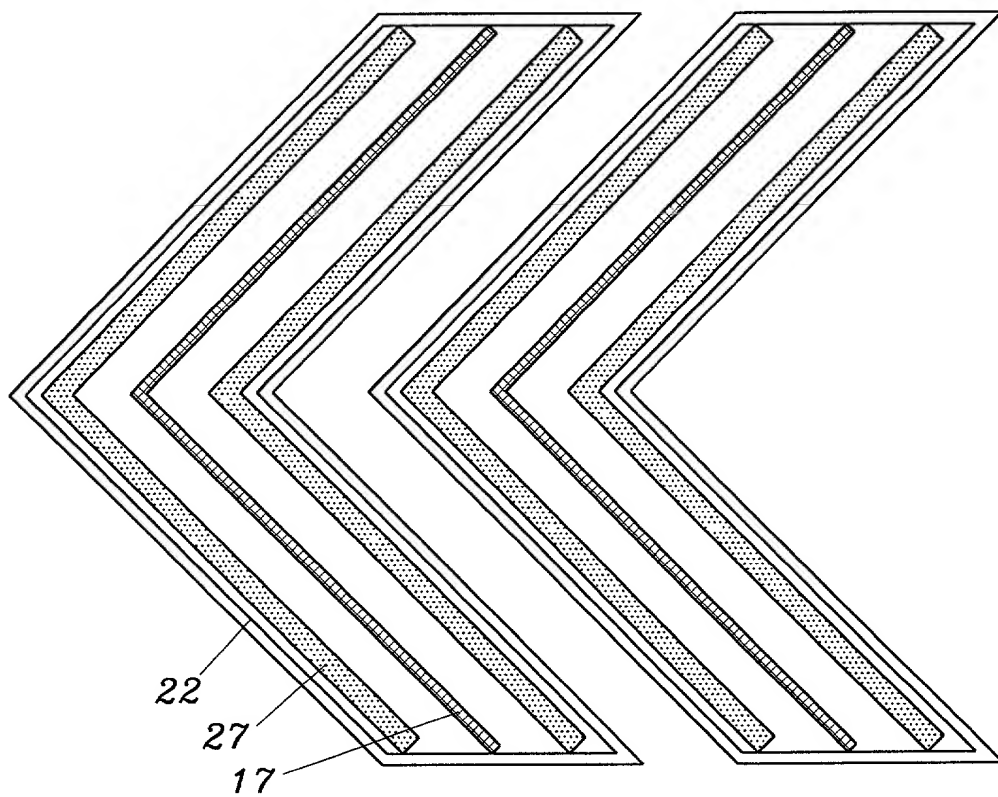


FIG. 28



Combined Declaration and Power of Attorney for Patent Application

Docket Number:

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter that is claimed and for which a patent is sought on the invention entitled

LIQUID CRYSTAL DISPLAY

, the specification of which is attached hereto unless the following box is checked:

☐ was filed on _____;
as United States Application Number or PCT International Application number _____;
and was amended on _____ (if applicable).

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information that is material to patentability as defined in 37 C.F.R. § 1.56.

I hereby claim foreign priority benefits under 35 U.S.C. § 119(a)-(d) or § 365(b) of any foreign application(s) for patent or inventor's certificate, or § 365(a) of any PCT international application, which designated at least one country other than the United States listed below, and have also identified below any foreign application for patent or inventor's certificate, or PCT international application having a filing date before that of the application on which priority is claimed.

Prior Foreign Application(s)			Priority Claimed
			<input checked="" type="checkbox"/> Yes <input type="checkbox"/> No
1999-42216 (Application No.)	KOREA (Country)	01/10/1999 (Day/Month/Year Filed)	

I hereby claim the benefit under 35 U.S.C. § 119(e) of any United States provisional application(s) listed below.

_____ (Application No.)	_____ (Filing Date)
----------------------------	------------------------

_____ (Application No.)	_____ (Filing Date)
----------------------------	------------------------

I hereby claim the benefit under 35 U.S.C. § 120 of any United States application(s), or under § 365(c) of any PCT international application designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT international application in the manner provided by the first paragraph of 35 U.S.C. § 112, I acknowledge the duty to disclose information that is material to patentability as defined in 37 C.F.R. § 1.56 that became available between the filing date of the prior application and the national or PCT international filing date of this application.

_____ (Application No.)	_____ (Filing Date)	_____ (Status - patented, pending, abandoned)
----------------------------	------------------------	--

I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith:

A 301 No.

- 2 -

Appl. No. _____
Docket No. _____

Full name of first inventor	SONG, Jang-Kun	
First inventor's signature	<i>Jang Jang Kun</i>	Date <i>Sept 27, 2000</i>
Residence	Seoul, Korea	
Citizenship	Republic of Korea	
Post Office Address	Samik Apt. 5-201, Seocho-dong, Seocho-ku, Seo il, Korea	

Full name of second inventor	RYU, Joo-Jin	
Second inventor's signature	<i>Joo Jin Ryu</i>	Date <i>Sep. 7, 2000</i>
Residence	Kyungki-do, Korea	
Citizenship	Republic of Korea	
Post Office Address	Yangbeol 1-ri 629-1, Opo-myeon, Kwangju-kun, Kyungki-do, Korea	